## DIGITALCLOCK DISTRBBUIOR

## DCD-400, DCD-ST2 AND DCD-CIM

## TESTAND ACCEPIANCE

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32. GENERAL
1.01 This practice provides test and acceptance procedures for the Digital Clock Distributor (DCD System). When acceptance testing has been completed, file the Sign-off sheet locally, as per local company practice.
1.02 This practice is reissued due to editorial changes. No change bars are used.
1.03 The following abbreviations are used in this document:

| ACI | Analog Clock Input card |
| :--- | :--- |
| AI | Alarm Interface card |
| CI | Clock Input card |
| DCD | Digital Clock Distributor |
| FA | Fuse and Alarm card |
| HS TOxA | Hot spare card |
| LOS | Loss of signal |
| MCA | Matrix Controller Automatic card |
| MCA-2 | Matrix Controller Automatic-2 card |
| OOF | Out of frame <br> SCIU |
| Synchronous Clock Insertion Unit card <br> ST2 | Stratum-2 Clock card |
| ST2E | Enhanced Stratum-2 Clock card <br> ST3 |
| Stratum-3 Clock card |  |

TOTA Timing Output T1 Automatic card TOxA Timing Output card

Note: Throughout this practice, information common to the CI and ACI cards is referenced as "clock input" or "input cards." Information unique to the CI is referred to as "CI;" the ACI is referred to as "ACI."

Note: Information common to the ST2, ST2E, and ST3E clock cards is referenced as "clock card" or "ST card." Information unique to the ST2 is referred to as "ST2;" the ST2E is referred to as "ST2E;" the ST3E is referred to as "ST3E;" the ST3 is referred to as "ST3."

Note: A lowercase " x " is used in card nomenclature as a variable to indicate all cards of that type (e.g., "TOxA" represents TOAA, TOCA, TOLA, and TOTA cards).
1.04 The DCD system consists of one master shelf and up to three expansion shelves. The DCD-ST2 shelf only operates as a master shelf. The DCD-400/ CIM may be used as either a master or an expansion shelf. A DCD-400/CIM may be an expansion shelf from a DCD-400/CIM or DCD-ST2 master shelf.

## 2. ACCEPTANCE TEST PROCEDURES

2.01 When performing any of the acceptance test procedures, if problems are encountered, or if requirements listed in a step are not met, refer to 097-4000-59 Maintenance to assist in troubleshooting. If the problem cannot be resolved, contact Telecom Solutions' Customer Technical Assistance Center (CTAC) at (408)433-0910 for help.
2.02 The acceptance testing should be performed after installing and connecting the master to expansion shelf(s), applying power to each shelf, and connecting reference inputs to the master shelf. The cabling from the office alarm system, remote telemetry equipment and timing outputs must not be terminated on the shelves prior to acceptance testing. After acceptance testing has been completed, return to TMSL 097-40000-57, DCD Installation, to make these connections.
2.03 To perform the acceptance test procedure, refer to Table A and Table B for test flow.

Table A. Master Shelf Test Fow

| STEP $^{*}$ | CHART TITLE |
| :---: | :--- |
| 1 | Power Test |
| 2 | Amplitude Verification Test |
| 3 | Al Card Test |
| 4 | FA Card Test |
| 5 | CI/ACI Card Test |
| 6 | ST2 Card Test |
| 7 | ST2E Card Test |
| 8 | ST3E Card Test |
| 9 | ST3 Card Test |

Table A. Master Shelf Test Row (Contd)

| STEP $^{*}$ | CHART TITLE |
| :---: | :--- |
| 10 | MCA/MCA-2 Card Test |
| 11 | Timing Output Card Test |
| 12 | SCIU Card Test |
| *If not equipped with a particular card, proceed to <br> the next step. |  |

Table B. Expansion Shelf Test How

| STEP* $^{*}$ | CHART TITLE |  |  |
| :---: | :--- | :---: | :---: |
| 1 | Power Test |  |  |
| 2 | FA Card Test |  |  |
| 3 | MCA/MCA-2 Card Test |  |  |
| 4 | Timing Output Card Test |  |  |
| 5 | SCIU Card Test |  |  |
| * If not equipped with a particular card, proceed to |  |  |  |
| the next step. |  |  |  |
|  |  |  |  |

## A. Power Test

2.04 This test assumes the DCD shelf has been physically and electrically installed per TMSL 097-40000-57, DCD Installation.

Caution: This test cannot be performed on a DCD shelf supplying timing to network elements. This test must be performed prior to using the DCD shelf to time network elements (NE). Failure to observe this caution will result in service interruption.

## Chart 1. Power Test

## STEP

## PROCEDURE

Use this procedure for verifying the power connections to the shelf. The DCD shelf does not load share the A and B battery feeds. The alternate source becomes active only if the active source fails. This procedure assumes power to the shelf under test has been connected per TMSL 097-40000-57, DCD Installation practice.

Test Equipment: Digital Multimeter, Fluke 77 or equivalent
Caution: Do not perform this procedure on DCD shelves which are supplying timing to network elements (in service). Failure to observe this caution will result in a service interruption.

| 1 | Ensure all plug-in cards are removed from the shelf under test. |
| :---: | :--- |
| 2 | Ensure all fuses are removed from the fuse panel which powers the shelf under test. |
| 3 | Disconnect the -48V A and -48V B power leads from the shelf power terminal blocks TB1 on the <br> rear of the shelf (leave the battery [RTN] leads connected to the shelf). |
| 4 | At the shelf end of the battery leads, use the multimeter to measure the voltage between the fol- <br> lowing: <br> a. Battery A lead and battery B lead <br> b. Battery A lead and battery return (RTN) terminal on the power terminal block |
| c. Battery A lead and frame (FRM) ground terminal on the power terminal block |  |
| d. Battery B lead and battery return (RTN) terminal on the power terminal block |  |
| e. Battery B lead and frame (FRM) ground terminal on the power terminal block |  |
| Requirement: The multimeter indicates no voltage between any of the points listed above. |  |

a. Battery A lead and battery B lead
b. Battery A lead and battery return (RTN) terminal on the power terminal block
c. Battery A lead and frame (FRM) ground terminal on the power terminal block
d. Battery B lead and battery return (RTN) terminal on the power terminal block
e. Battery B lead and frame (FRM) ground terminal on the power terminal block

Requirement: The multimeter indicates infinite resistance (completely open circuit).

## Chart 1. Power Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 6 | Reconnect the A and B battery leads to the power terminal block (TB1) -48V A and -48V B termi- <br> nal sets on the shelf under test. |
| 7 | Verify that the shelf will cause an alarm if all power is lost to the shelf by connecting the mul- <br> timeter, set to ohms, across the following terminal sets on the shelf backplane (Figure 3). <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6)) <br> MINOR VIS NO and C (TB2, pins 2 \& 3 6) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) |
| Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATT ALM and RTN (TB15, pins 1 \& 2 - DCD-ST2 shelf only) <br> Requirement: The multimeter indicates <15 ohms on all terminal sets. |  |
| 8 | In the power source fuse panel, install the A and B battery fuses for the shelf under test. Use <br> 7.5 A to 10 A size fuses for the DCD-ST2 Shelf or 3A size fuses for DCD-400/CIM Shelf. |
| 9 | Use the multimeter to measure the voltage between the -48V A and RTN terminal sets on the <br> power terminal block (TB1) on the shelf under test. <br> Requirement: The multimeter indicates -42 to -56 volts dc. |
| 10 | Use the multimeter to measure the voltage between the -48V B and RTN terminal sets on the <br> power terminal block (TB1) on the shelf under test. <br> Requirement: The multimeter indicates -42 to -56 volts dc. |

## Chart 1. Power Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 11 | Verify that the shelf will not cause a battery alarm if there is power to the shelf from both <br> sources by connecting the multimeter, set to maximum ohms, across the following terminal sets <br> on the shelf backplane (Figure 3). <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) |
| Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATT ALM and RTN (TB15, pins 1 \& 2) - DCD-ST2 shelf only <br> Requirement: The multimeter indicates infinite resistance (completely open circuit) on all <br> terminal sets. <br> 12Repeat Steps 1 through 11 for each of the remaining shelves (if any) in the DCD System. |  |
| 13 | This procedure is completed. Indicate completion of the Power Test on the Test Sign-off form, <br> then proceed to next section. |

## B. Amplitude Verification

2.05 Use this procedure for verifying that clock input signal(s) are within the specified input voltage range. This procedure assumes input to the shelf under test has been connected per TMSL 097-4000057, DCD Installation.

## Chart 2. Amplitude Verific ation Test

| STEP | PROCEDURE |
| :---: | :--- |
| Test Equipment: Dual-channel oscilloscope with 100 MHz minimum bandwidth |  |
| 1 | Obtain a dual-channel oscilloscope and preset the controls as listed in Table C. |
| 2 | Connect X10 probes to channel 1 and channel 2 oscilloscope inputs and install a terminating <br> resistor across the probes. Connect channel 1 and 2 probe ground leads together. Connect <br> channel 1 and 2 probes (with resistor) to T and R of each DCD shelf input terminals. |
| 3 | Verify that the clock input signal(s) are within the specified input voltage range and meets the <br> prescribed template. Consult local company Installation Job Specifications for the type of input <br> signals installed and on which DCD input connectors. <br> Requirement: Input signal ranges are as follows: |
| a. DS1 Terminated: 1.0 to 3.5 V base-to-peak, 100 ohms terminated |  |
| (TB7 and TB10, pins 1 \& 2) |  |
| b. DS1 Bridged: 0.10 to 0.35 V base-to-peak, 100 ohms terminated |  |
| (TB6 and TB9, pins 1 \& 2) |  |

Table C. Typic al Oscillosc ope Settings*

| CONTROL | SETTING |
| :---: | :---: |
| Mode Trigger Switch | ADD |
| Invert Switch | INVERT |
| Ch1 and Ch2: <br> Volts/Div <br> Input Coupling <br> Vert Position Control | 5 <br> AC <br> mid-range |
| "A" Sweep Mode | AUTO-TRIGGER |
| Horizontal Display Switch | "A" |
| Time/Div Switch: <br> for CC (TOCA) <br> for $1.544 \mathrm{Mb} / \mathrm{s}$ (TOTA) <br> for Analog (TOAA) <br> for Logic Level (TOLA) | $20 \mu \mathrm{~s}$ <br> 500 ns <br> $60 \mathrm{kHz}: 10 \mu \mathrm{~s}$ <br> $4 \mathrm{~kb} / \mathrm{s}: 0.5 \mathrm{~ms}$ <br> $8 \mathrm{~kb} / \mathrm{s}: 0.1 \mathrm{~ms}$ <br> $64 \mathrm{~kb} / \mathrm{s}: 10 \mu \mathrm{~s}$ <br> $564 \mathrm{~kb} / \mathrm{s}: 2 \mu \mathrm{~s}$ <br> $384 \mathrm{~kb} / \mathrm{s}$ : $2 \mu \mathrm{~s}$ <br> $512 \mathrm{~kb} / \mathrm{s}: 2 \mu \mathrm{~s}$ <br> $768 \mathrm{~kb} / \mathrm{s}$ : $2 \mu \mathrm{~s}$ <br> $1.536 \mathrm{Mb} / \mathrm{s}: 0.5 \mu \mathrm{~s}$ <br> $1.5446 \mathrm{Mb} / \mathrm{s}: 0.5 \mu \mathrm{~s}$ <br> $2.048 \mathrm{Mb} / \mathrm{s}: 0.1 \mu \mathrm{~s}$ |
| "A" Sweep | FULL |
| Horizontal Position Control | mid-range |
| " A " Triggering: <br> Slope <br> Coupling Source | $\begin{aligned} & + \text { or - } \\ & \text { AC } \\ & \text { INT } \end{aligned}$ |
| * Consult the oscilloscope manufacturer's manual for details and operating instructions. |  |

## C. Card Test

## Warning: When handling cards, use local

 office procedures regarding electrostatic discharge (ESD), including the following:- Use grounded wrist straps connected to equipment frame ground when handling cards.
- Store cards only in antistatic packaging provided by the factory.
2.06 System circuit cards are keyed, making it virtually impossible to fully insert a card into the wrong slot. If a card does not seat fully, make sure the card is sliding into the proper slot. Labels printed along the top lip of the shelves identify each slot. Use the following instructions whenever installing a card into a slot:

1. Hold the card by the locking lever on the faceplate with one hand, and carefully hold and align card with the shelf card edge guides with the other hand. Gently seat the card in the connector on the shelf backplane.

Note: Always use two hands to install cards, especially ST2 and ST2E clock cards.
2. Secure the card into place by lowering the locking lever.
2.07 Each shelf has card slots for TOxA, SCIU, ACI, MCA/MCA-2, CI, FA, AI, ST3, ST3E, ST2, and ST2E cards. Refer to Figure 1 and Figures 2a and $2 b$ for shelf card slot positions.


Notes:

1. TOxA and SCIU cards may be intermixed in the output slots on the shelf; the SCIU cards are unprotected cards and do not require Hot Spare (HS) cards.
2. If configured for all unprotected output, an MCA/MCA-2 card is not required; the slot cannot be used as an unprotected output slot.

Figure 1. DCD-ST2 Shelf Card Slot Positions (Front View)

a. DCD-400 Shelf Card


Note: Although the CIM can be used as a master shelf (in a standalone configuration), it is not recommended. The CIM does not provide protection switching.

## b. DCD-CIM Shelf Card

Figure 2. Shelf Card Slot Positions (Front View)

Chart 3. FA Card Test (DCD-400/CIM, Master and Expansion Shelves Only)

## STEP <br> PROCEDURE

Use this procedure to install the FA card and verify operation. If the FA card is not used, skip this chart and proceed to the next chart. This procedure assumes power has been applied to the shelf and alarm connections have been made per TMSL 097-40000-57, DCD Installation practice.

## Notes:

1. There are no switch or jumper settings for the FA card.
2. When checking relay contact status, refer to Figure 3 b for connector locations, and Figure 3c for pin locations.
3. This test assumes that the alarm system requires normally open (NO) for a no alarm condition; if local company practice dictates that the alarm system requires normally closed (NC) for no alarm, refer to Figure 3c and test normally closed pins.
4. If the Office Alarm and Shelf Status terminal sets have been connected to the office alarm system and remote telemetry equipment, the ohmmeter tests cannot be performed because these systems apply -48 V on the alarm leads and ground on the return leads. To adequately test the FA card, perform one of the following:
a. Disconnect the office alarm system and remote telemetry equipment from the DCD-400/CIM shelf, and perform the ohmmeter tests, then reconnect the leads.
b. Leave the office alarm system and remote telemetry equipment connections intact, and substitute the ohmmeter tests with audible and visual tests for appropriate condition responses from the office alarm system and verification of appropriate condition responses at the alarm and surveillance center.

| 1 | Insert the FA card into the slot labeled "FA" in the DCD-400 or DCD-CIM shelf. <br> Requirement: The MAJOR and MINOR lamps on the FA card are off. |
| :---: | :--- |
| 2 | Remove fuse "-48V A" on the front panel of the FA card with a small, flat-blade screwdriver. <br> Requirement: The red lamp beside the -48V A fuse holder on the FA card lights, and the <br> MINOR lamp lights. |
| 3 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3b): <br> Office Alarm: |
| MINOR AUD NO and C (TB2, pins $5 \& 6)$ <br> MINOR VIS NO and C (TB2, pins $2 \& 3)$ |  |
| $\frac{\text { Shelf Status: }}{\text { MINSI and MINSR (TB2, pins 7 \& 8) }}$Requirement: The multimeter indicates <15 ohms across each set of terminals, indicating <br> that the contacts have closed. |  |

Chart 3. FA Card Test (DCD-400/CIM, Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 4 | Press the ACO pushbutton on the FA card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the FA card lights green, and the MINOR and -48V A lamps <br> remain lit. |
| 5 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3b); <br> Office Alarm: |
| MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) <br> Shelf Status: |  |
| 6 | MINSI and MINSR (TB2, pins 7 \& 8) <br> Requirement: The MINORAUD NO and C contacts are open (infinite ohms); the MINOR VIS <br> NO and C, and MINSI and MINSR contacts remain closed (<15 ohms). |
| 7 | Replace the -48V A fuse using a small flat-blade screwdriver to lock in place. <br> Requirement: -48V A fuse lamp and FA MINOR and ACO lamps go off (after approximately <br> 6 to 10 seconds). |
| Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3b); <br> Office Alarm: |  |
| MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) <br> Shelf Status: |  |
| MINSI and MINSR (TB2, pins 7 \& 8) |  |
| Requirement: All alarm contacts are open (infinite ohms). |  |$|$

Chart 3. FA Card Test (DCD-400/CIM, Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 9 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3b); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> Requirement: The multimeter indicates $<15 \mathrm{ohms}$ across each set of terminals, indicating that the contacts have closed. |
| 10 | Press the ACO pushbutton on the FA card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the FA card lights green; and the MINOR and -48V B lamps remain lit. |
| 11 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> Requirement: The MINOR AUD NO and C contacts are open (infinite ohms); and MINOR VIS NO and C, and MINSI and MINSR contacts remain closed ( $<15 \mathrm{ohms}$ ). |
| 12 | Replace the -48 V B fuse using a small flat-blade screwdriver to lock in place. <br> Requirement: -48V B fuse lamp, FA MINOR and ACO lamps go off (after approximately 6 to 10 seconds). |
| 13 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3c): <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins $7 \& 8$ ) <br> Requirement: All alarm contacts are open (infinite ohms). |
| 14 | Remove both fuses from the front of the FA card using a small, flat-blade screwdriver. <br> Requirement: All lamps on the shelf will go out. |

Chart 3. FA Card Test (DCD-400/CIM, Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 15 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3c): <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3 <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> Requirement: All alarm contacts are closed ( $<15 \mathrm{ohms}$ ). |
| 16 | Replace both fuses using a small flat-blade screwdriver to lock in place. <br> Requirement: The MAJOR, MINOR, and ACO lamps are off on the FA card, and the -48V A and -48 V B lamps are off on the Output Protection/Spare Select panel. |
| 17 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3c): <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins $5 \& 6$ ) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3 <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> Requirement: All alarm contacts are open (infinite ohms). |
| 18 | Insert a TOxA card in any TOx output slot in the shelf. <br> Requirement: FAIL lamp lit on TOxA card, MAJOR lamp lit on FA card. |
| 19 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3c); <br> Office Alarm: <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> Requirement: The multimeter indicates $<15$ ohms across each set of terminals, indicating that the contacts have closed. |

Chart 3. FA Card Test (DCD-400/CIM, Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 20 | Press the ACO pushbutton on the FA card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the FA card lights green, and the MAJOR and -48V A lamps <br> remain lit. |
| 21 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3c); <br> Office Alarm: |
| MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: |  |
| MAJSI and MAJSR (TB3, pins 7 \& 8) <br> Requirement: The MAJOR AUD NO and C are open (infinite ohms); and the MAJSI and <br> MAJSR, MAJOR VIS NO and C remain closed (<15 ohms). |  |
| 22 | Remove the TOxA card inserted in Step 18. <br> Requirement: MAJOR lamp on FA card goes off in 6 to 10 seconds. |
| 23 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3c); <br> Office Alarm: |
| MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> Requirement: All alarm contacts are open (infinite ohms). |  |
| 24 | This completes the tests for FA card MINOR and MAJOR alarms. Indicate completion of the <br> FA Card Test on the Test Sign-off form, then proceed to the next chart. |



Figure 3. Shelf Backplane Connector Positions (Rear View)

## Chart 4. AI Card Test (DCD-ST2 Master Shelf Only)

## STEP

PROCEDURE
Use this procedure to install the AI card and verify operation. If the AI card is not used, skip this chart and proceed to the next chart. This procedure assumes power has been applied to the shelf and alarm connections have been made per TMSL 097-40000-57, DCD Installation practice.

## Notes:

1. There are no switch or jumper settings for the AI card.
2. When checking relay contact status, refer to Figure 3a for connector locations, and Figure 3c for pin locations.
3. This test assumes that the alarm system requires normally open ( NO ) for a no alarm condition; if local company practice dictates that the alarm system requires NC (Normally Closed) for no alarm, refer to Figure 3c and test normally closed pins.
4. If the Office Alarm and Shelf Status terminal sets have been connected to the office alarm system and remote telemetry equipment, the ohmmeter tests cannot be performed because these systems apply -48 V on the alarm leads and ground on the return leads. To adequately test the AI card, perform one of the following:
a. Disconnect the office alarm system and remote telemetry equipment from the DCD-ST2 shelf, and perform the ohmmeter tests, then reconnect the leads.
b. Leave the office alarm system and remote telemetry equipment connections intact, and substitute the ohmmeter tests with audible and visual tests for appropriate condition responses from the office alarm system and verification of appropriate condition responses at the alarm and surveillance center.

| 1 | Insert the AI card into the slot labeled "AI" in the DCD-ST2 master shelf. <br> Requirement: The MAJOR and MINOR lamps on the AI card are off. |
| :---: | :--- |
| 2 | Set the POWER ALARM switch (SW11) on the DCD-ST2 shelf backplane to MIN (see <br> Figure 3a). |
| 3 | Remove fuse "-48V A" from the Output Protection/Spare Select Panel on the DCD-ST2 shelf <br> with a small flat-blade screwdriver. |
| Requirement: The red lamp under the -48V A fuse on the Output Protection/Spare Select <br> Panel front panel lights, and the MINOR lamp on the AI card lights. |  |

## Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 4 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a): <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The multimeter indicates $<15$ ohms across each set of terminals, indicating that the contacts have closed. |
| 5 | Press the ACO pushbutton on the AI card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the AI card lights green, and the MINOR and -48V A lamps remain lit. |
| 6 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The MINOR AUD NO and C contacts are open (infinite ohms); the MINOR VIS NO and C, MINSI and MINSR, and BATTALM and BATTRTN remain closed ( $<15 \mathrm{ohms}$ ). |
| 7 | Replace the -48 V A fuse using a small flat-blade screwdriver to lock in place. <br> Requirement: -48V A fuse lamp and AI MINOR and ACO lamps go off (after approximately 6 to 10 seconds). |

## Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 8 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins $5 \& 6$ ) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: All alarm contacts are open (infinite ohms). |
| 9 | Remove fuse "-48V B" from the Output Protection/Spare Select Panel on the DCD-ST2 shelf using a small flat-blade screwdriver. <br> Requirement: The red lamp under the -48 V B fuse on the Output Protection/Spare Select Panel front panel lights, and the MINOR lamp on the AI card lights. |
| 10 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The multimeter indicates $<15$ ohms across each set of terminals, indicating that the contacts have closed. |
| 11 | Press the ACO pushbutton on the AI card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the AI card lights green, and the MINOR and -48V B lamps remain lit. |

## Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 12 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: |
| MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) <br> Shelf Status: |  |
| MINSI and MINSR (TB2, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins 1 \& 2) |  |
| Requirement: The MINOR AUD NO and C contacts are open (infinite ohms), and MINOR <br> VIS NO and C, MINSI and MINSR, and BATTALM and BATTRTN remain closed (<15 ohms). |  |
| 13 | Replace the -48V B fuse using a small flat-blade screwdriver to lock in place. <br> Requirement: -48V B fuse lamp, AI MINOR and ACO lamps go off (after approximately 6 to <br> 10 seconds). |
| Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: |  |
| MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins 2 \& 3) |  |
| $\frac{\text { Shelf Status: }}{}$MINSI and MINSR (TB2, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins 1 \& 2) |  |
| Requirement: All alarm contacts are open (infinite ohms). |  |$|$| Set the POWER ALARM switch (SW11) on the shelf backplane to MAJ (see Figure 3a). |
| :--- |

## Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 17 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The multimeter indicates $<15$ ohms across each set of terminals, indicating that the contacts have closed. |
| 18 | Press the ACO pushbutton on the AI card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the AI card lights green, and the MAJOR, MINOR, and -48V A lamps remain lit. |
| 19 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The MAJOR and MINOR AUD NO and C are open (infinite ohms); and the MAJSI and MAJSR, MAJOR and MINOR VIS NO and C, MINSI and MINSR, and BATTALM and BATTRTN remain closed ( $<15 \mathrm{ohms}$ ). |
| 20 | Replace the -48 V A fuse using a small flat-blade screwdriver to lock in place. <br> Requirement: -48V A fuse lamp and AI MAJOR, MINOR, and ACO lamps go off (after approximately 6 to 10 seconds). |

# Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd) 

| STEP | PROCEDURE |
| :---: | :---: |
| 21 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: All alarm contacts are open (infinite ohms). |
| 22 | Remove fuse "-48V B" from the Output Protection/Spare Select Panel on the DCD-ST2 shelf using a small flat-blade screwdriver. <br> Requirement: The red lamp under the -48 V B fuse on the Output Protection/Spare Select Panel front panel lights, and the MAJOR and MINOR lamps light on the AI card. |
| 23 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The multimeter indicates $<15$ ohms across each set of terminals, indicating that the contacts have closed. |
| 24 | Press the ACO pushbutton on the AI card to silence the audible office alarm (if connected). <br> Requirement: The ACO lamp on the AI card lights green, and the MAJOR, MINOR, and -48V B lamps remain lit. |

## Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 25 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins $5 \& 6$ ) <br> MAJOR VIS NO and C (TB3, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins $7 \& 8$ ) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: The MAJOR and MINOR AUD NO and C are open (infinite ohms); and the MAJSI and MAJSR, MAJOR and MINOR VIS NO and C, MINSI and MINSR, and BATTALM and BATTRTN remain closed ( $<15 \mathrm{ohms}$ ). |
| 26 | Replace the -48 V B fuse using a small flat-blade screwdriver to lock in place. <br> Requirement: -48V B fuse lamp, AI MAJOR, MINOR, and ACO lamps go off (after approximately 6 to 10 seconds). |
| 27 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c); <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins $2 \& 3$ ) <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: All alarm contacts are open (infinite ohms). |

# Chart 4. AI Card Test (DCD-ST2 Master Shelf Only) (Contd) 

| STEP | PROCEDURE |
| :---: | :---: |
| 28 | Remove both fuses from the Output Protection/Spare Select Panel on the DCD-ST2 shelf using a small flat-blade screwdriver. <br> Requirement: All lamps on the shelf are off, except the -48 V A and -48 V B lamps on the Output Protection/Spare Select panel, and the MAJOR, MINOR, and ACO lamps on the AI card, which are lit. |
| 29 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c): <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins $5 \& 6$ ) <br> MAJOR VIS NO and C (TB3, pins $2 \& 3$ <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: All alarm contacts are closed ( $<15 \mathrm{ohms}$ ). |
| 30 | Replace both fuses using a small flat-blade screwdriver to lock in place. <br> Requirement: The MAJOR, MINOR, and ACO lamps are off on the AI card, and the -48V A and -48 V B lamps are off on the Output Protection/Spare Select panel. |
| 31 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status terminal sets on the shelf backplane (see Figure 3a and Figure 3c): <br> Office Alarm: <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3 <br> Shelf Status: <br> MINSI and MINSR (TB2, pins 7 \& 8) <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> BATTALM and BATTRTN (TB15, pins $1 \& 2$ ) <br> Requirement: All alarm contacts are open (infinite ohms). |
| 32 | This completes the tests for AI card MINOR and MAJOR alarms. Indicate completion of the AI Card Test on the Test Sign-off form, then proceed to the next chart. |

## Chart 5. CI/ACI Card Test

| STEP | PROCEDURE |
| :---: | :--- |
| $\begin{array}{l}\text { Use this procedure to verify the operation of the CI/ACI cards. If the CI/ACI cards are not used, skip this } \\ \text { chart and proceed to the next chart. This procedure assumes the clock input signals have been connected per } \\ \text { TMSL 097-40000-57, DCD Installation practice. Consult the local company Installation Job Specifications to } \\ \text { determine the type of input card, either CI or ACI, to be installed in the CI-A and CI-B slots in the shelf. }\end{array}$ |  |
| 1 | $\begin{array}{l}\text { If the DCD-400/CIM shelf is the master shelf, skip this step. On the rear of the DCD-ST2 master } \\ \text { shelf, set the ST3/ST2 switch (SW1) to the ST3 position (Figure 3a). }\end{array}$ |
| 2 | $\begin{array}{l}\text { If not equipped with CI cards, skip to Step 6. If equipped with a CI card, set sections 1 through 4 } \\ \text { of switch SW1 (Figure 4) on the CI card as required for the type of clock input signal (refer to } \\ \text { local company Installation Job Specifications for the type of clock input signals). }\end{array}$ |
| 3 | $\begin{array}{l}\text { If not equipped with a second CI card, skip to the next step. If equipped with a second CI card, } \\ \text { repeat the previous step for the second CI card. }\end{array}$ |
| 4 | $\begin{array}{l}\text { If the CI-A slot in the master shelf is to be equipped with a CI card, insert a CI card into this slot. } \\ \text { If not equipped with a CI card, skip to the next step. } \\ \text { Requirement: On the CI card just installed, the DS1 lamp is lit green, and the FAIL lamp } \\ \text { goes off after approximately 8 to 40 seconds. After the card has warmed up, the SRC ACTIVE } \\ \text { lamp lights green. }\end{array}$ |
| 5 | $\begin{array}{l}\text { If the CI-B slot in the master shelf is to be equipped with a CI card, insert a CI card into this slot. } \\ \text { If not equipped with a CI card, skip to the next step. }\end{array}$ |
| 7 | $\begin{array}{l}\text { Requirement: On the CI card just installed, the DS1 lamp is lit green, and the FAIL lamp } \\ \text { goes off after approximately 8 to 40 seconds. If this is the first input card installed, after the } \\ \text { card has warmed up, the SRC ACTIVE lamp lights green. If this is the second input card, the } \\ \text { SRC ACTIVE lamp will remain off. }\end{array}$ |
| 6 | $\begin{array}{l}\text { If not equipped with ACI cards, skip to Step 10. If equipped with an ACI card, set sections 1 and } \\ 2 \text { of switch SW4 4 Figure 5) to match the frequency of the clock input signal (refer to local com- } \\ \text { pany Installation Job Specifications for the frequency of the clock input signals). }\end{array}$ |
| 9 | $\begin{array}{l}\text { If not equipped with a second ACI card, skip to the next step. If equipped with a second ACI } \\ \text { card, repeat the previous step for the second ACI card. }\end{array}$ |
| $\begin{array}{l}\text { If the CI-A slot in the master shelf is to be equipped with an ACI card, insert an ACI card into } \\ \text { this slot. If not equipped with an ACI card, skip to the next step. }\end{array}$ |  |
| Requirement: On the ACI card just installed, the FL lamp and SRC FL lamps are lit red, but |  |
| boqth lamps go off approximately 20 seconds after the card is installed, and the SRC ACT lamp |  |
| lights green. |  |$\}$

## Chart 5. CI/ ACI Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 10 | Press the XFR pushbutton on one of the CI/ACI cards. <br> Requirement: The lit SRC ACT/SRC ACTIVE lamp indication transfers to the other CI/ACI card. |
| 11 | Press the XFR pushbutton on the other CI/ACI card. <br> Requirement: The lit SRC ACT/SRC ACTIVE lamp indication transfers back to the original CI/ACI card. |
| 12 | Remove the CI/ACI card with its SRC ACT/SRC ACTIVE lamp lit. <br> Requirement: The SRC ACT/SRC ACTIVE lamp lights on the other CI/ACI card. |
| 13 | Reinsert the removed CI/ACI card. <br> Requirement: The SRC ACT/SRC ACTIVE lamp remains lit on the other CI/ACI card. |
| 14 | Remove the CI/ACI card with its SRC ACT/SRC ACTIVE lamp lit. <br> Requirement: The SRC ACT/SRC ACTIVE lamp on the original CI/ACI card lights. |
| 15 | Reinsert the removed CI/ACI card. <br> Requirement: The SRC ACT/SRC ACTIVE lamp remains lit on the other CI/ACI card. |
| 16 | Press the XFR pushbutton on the other CI/ACI card to transfer active status to the CI/ACI A card. <br> Requirement: The lit SRC ACT/SRC ACTIVE lamp indication transfers back to the other CI/ACI card. |
| 17 | If the DCD-ST2 shelf is to be equipped with ST2 or ST2E clock cards, set the ST3/ST2 switch (SW1) on the shelf backplane to the ST2 position (Figure 3a). If the shelf is to be equipped with ST3E or ST3 clock cards, leave SW1 in the ST3 position. If the DCD-400/CIM shelf is the master shelf, skip this step. <br> Requirement: If SW1 is set to the ST2 position, the SRC ACT/SRC ACTIVE lamps on both CI/ACI cards will be lit. The transfer (XFR) function between the CI/ACI cards will be disabled. |
| 18 | This procedure is completed. Indicate completion of the CI/ACI Card Test on the Test Sign-off form, then proceed to next chart. |



Figure 4. CI Card Switch


Figure 5. ACI Card Switch

## Chart 6. ST2 Card Test (DCD-ST2 Master Shelf Only)

| STEP | PROCEDURE |
| :--- | :--- |
| Use this procedure to verify the operation of the ST2 cards. If ST2 cards are not used, or if the master shelf is <br> a DCD-400/CIM Shelf, skip this chart. This procedure assumes the procedure for the CI/ACI cards has been <br> completed and there are input references connected to the shelfs input terminals. |  |
| There are no switch options on the ST2 card. |  |
| Notes: <br> 1. The times indicated in the procedure are from the time the card is powered (inserted in the shelf). <br> 2. If the ST2 card is Issue D or earlier, the oscillator stabilization period is approximately 1 hour and is <br> software controlled (not a function of oscillator temperature). If Issue E or later, the rubidium temper- <br> ature is checked upon insertion and the oscillator may stabilize in less than one hour. <br> 3. If the FAIL lamp lights on an ST2 card at any time, the card has failed and must be replaced. |  |
| 1 | On the DCD-ST2 master shelf backplane, set the ST2/ST3 switch (SW1) to the ST2 position <br> (Figure 3a). <br> Requirement: Both clock input cards' SRC ACT/SRC ACTIVE lamps are lit. |
| 2 | In the master shelf, insert the first ST2 card into the ST A slot. |
| 3 | In the master shelf, insert the second ST2 card into the ST B slot. <br> Note: There is not a waiting period between installation of the first ST2 card and the sec- <br> ond ST2 card. |
| 4 | During the stabilization period (approximately 40 - 60 minutes), observe the FREE RUN lamp <br> on both ST2s. |
| Requirement: On both ST2 cards, the FREE RUN lamp flashes green. |  |

Chart 6. ST2 Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 7 | Connect the multimeter, set to ohms, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and FREERUN (TB11, pins 5 \& 6) <br> - CLOCK STATUS A RTN and LOCKED (TB11, pins 9 \& 10) <br> - CLOCK STATUS B RTN and FREERUN (TB12, pins 5 \& 6) <br> - CLOCK STATUS B RTN and LOCKED (TB12, pins 9 \& 10) <br> Requirements: Both A and B FREERUN and LOCKED to RTN are closed ( $<15 \mathrm{ohms}$ ). <br> Note: Ohms cannot be measured across terminal sets if remote telemetry equipment is connected to the terminal sets. |
| 8 | Reinsert both clock input cards. <br> Requirement: After approximately 8-40 seconds, the SRC ACT/SRC ACTIVE lamps on the clock input cards light, and the appropriate REF lamps light on the ST2 cards. |
| 9 | Connect the multimeter, set to ohms, across the following terminal sets on the shelf backplane. <br> - CLOCK STATUS A RTN and FREERUN (TB11, pins 5 \& 6) <br> - CLOCK STATUS B RTN and FREERUN (TB12, pins 5 \& 6) <br> Requirements: Both A and B FREERUN to RTN are open (infinite ohms). <br> Note: Ohms cannot be measured across terminal sets if remote telemetry equipment is connected to the terminal sets. |
| 10 | Approximately 5 minutes (up to 20 minutes for worst-case signal conditions) after the end of the stabilization period, observe the LOCKED and ACTIVE lamps on the ST2 cards. <br> Requirement: On both ST2 cards, the LOCKED lamp lights green. On one of the ST2 cards, the ACTIVE lamp is lit green. |
| 11 | Connect the multimeter, set to ohms, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and LOCKED (TB11, pins 9 \& 10) <br> - CLOCK STATUS B RTN and LOCKED (TB12, pins 9 \& 10) <br> Requirements: Both A and B LOCKED to RTN are open (infinite ohms). <br> Note: Ohms cannot be measured across terminal sets if remote telemetry equipment is connected to the terminal sets. |
| 12 | Press the XFR pushbutton on one of the ST2 cards. <br> Requirement: The lit ACTIVE lamp indication transfers to the other ST2 card, the LOCKED lamps remain lit. |

## Chart 6. ST2 Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 13 | Press the XFR pushbutton on the other ST2 card. <br> Requirement: The lit ACTIVE lamp indication transfers back to the original card, the LOCKED lamps remain lit. |
| 14 | If the ACTIVE lamp on ST2 A is not lit, press the XFR pushbutton on its faceplate to make it active. Remove the clock input A card. <br> Requirement: The ACTIVE lamp on the ST2 A card remains lit, the lit REF A lamp goes off and the REF B lamp lights to indicate that the card is now receiving input reference from clock input B card. The LOCKED lamp may go off, if so, it will relight after approximately 5 minutes. |
| 15 | Remove the ST2 A card. <br> Requirement: The ACTIVE lamp on the ST2 B card lights. |
| 16 | Reinsert the ST2 A and clock input A cards. <br> Requirement: After the stabilization period (approx. 5-60 minutes), the SRC ACT/SRC ACTIVE lamps on the clock input A card, and the REF A and LOCKED lamps on the ST2 A card light, and the ACTIVE lamp on the ST2 B card remains lit. |
| 17 | If the ACTIVE lamp on ST2 B is not lit, press the XFR pushbutton on its faceplate to make it active. Remove the clock input B card. <br> Requirement: The ACTIVE lamp on the ST2 B card remains lit, the lit REF B lamp goes off and the REF A lamp lights to indicate that the card is now receiving input reference from clock input A card. The LOCKED lamp may go off, if so, it will relight after approximately 5 minutes. |
| 18 | Remove the ST2 B card. <br> Requirement: The ACTIVE lamp on the ST2 A card lights. |
| 19 | Reinsert the ST2 B and clock input B cards. <br> Requirement: After the stabilization period (approx. 5-60 minutes), the SRC ACT/SRC ACTIVE lamps on the clock input B card and the REF B and LOCKED lamps on the ST2 B card light, and the ACTIVE lamp on the ST2 A card remains lit. |
| 20 | Remove both clock input cards. <br> Requirement: On both ST2 cards the REF and LOCKED lamps go off, the ACTIVE lamp remains lit on the ST2 B card, and the red HOLDOVER lamps light on both ST2 cards. |

Chart 6. ST2 Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 21 | Connect the multimeter, set to volts dc scale, across the following terminal sets on the shelf <br> backplane: <br> - CLOCK STATUS A RTN and HOLDOVER (TB11, pins 7 \& 8) <br> - CLOCK STATUS B RTN and HOLDOVER (TB12, pins $7 \& 8$ ) <br> Requirements: Both A and B HOLDOVER to RTN read <0.1 volts dc. |
| 22 | Note: The HOLDOVER and RTN status terminal set is connected to -48V and battery re- <br> turn through relay windings for the holdover MAJOR/MINOR option switch (SW11) oper- <br> ation. Therefore, ohms cannot be measured across this terminal set. |
| Reinsert both clock input cards. <br> Requirement: The REF lamps light on the clock input cards, REF A on the ST2 A, and REF B <br> on the ST2 B lights; the LOCKED lamp on both ST2 cards light and the HOLDOVER lamp on <br> both ST2 cards go out. |  |
| 23 | Connect the multimeter, set to volts dc scale, across the following terminal sets on the shelf <br> backplane: <br> - CLOCK STATUS A RTN and HOLDOVER (TB11, pins $7 \& 8$ ) <br> - CLOCK STATUS B RTN and HOLDOVER (TB12, pins 7 \& 8) |
| Requirements: Both A and B HOLDOVER to RTN read -48 volts dc. |  |
| 24 | Note: The HOLDOVER and RTN status terminal set is connected to -48V and battery re- <br> turn through relay windings for the holdover MAJOR/MINOR option switch (SW11) oper- <br> ation. Therefore, ohms cannot be measured across this terminal set. |
| This procedure is completed. Indicate completion of the ST2 Card Test on the Test Sign-off form, <br> then proceed to next chart. |  |

## Chart 7. ST2E Card Test (DCD-ST2 Master Shelf Only)

STEP PROCEDURE

Use this procedure to verify the operation of the ST2E cards. If ST2E cards are not used, or if the master shelf is a DCD-400/CIM shelf, skip this chart. This procedure assumes the procedure for the CI/ACI cards has been completed and there are input references connected to the shelfs input terminals.

There are no switch options on the ST2E card.
Notes:

1. The times indicated in the procedure are from the time the card is powered (inserted in the shelf).
2. If the FAIL lamp lights on an ST2E card at any time, the card has failed and must be replaced.

Note: Do not press the DSBL pushbutton switch on the ST2E A card unless that card is going to be removed from the shelf since its output will be disabled for up to 20 minutes. The DSBL pushbutton on the ST2E B card in slot B does not function (ST2E B cannot be disabled).

| 1 | On the DCD-ST2 master shelf backplane, set the ST2/ST3 switch (SW1) to the ST2 position <br> (Figure 3a). <br> Requirement: Both clock input cards' SRC ACT/SRC ACTIVE lamps are lit. |
| :---: | :--- |
| 2 | In the master shelf, insert the first ST2E card into the ST A slot. <br> Requirement: HOLDOVER lamp flashes green during the approximately 30 minutes stabi- <br> lization period. |
| 3 | In the master shelf, insert the second ST2E card into the ST B slot. <br> Note: No waiting period is necessary between when the first ST2E card and the second <br> ST2E card may be installed. |
| 4 | Requirement: HOLDOVER lamp flashes green during the approximately 30 minutes stabi- <br> lization period. |
| After the stabilization period, observe the HOLDOVER, SRC A, and SRC B lamps. <br> Requirement: On both ST2E cards, the HOLDOVER lamp stops flashing, and the SRC A <br> lamp lights green on the ST2E A card, and the SRC B lamp lights green on the ST2E B card. |  |
| Note: If an ST2E does not recognize its associated SRC input (ST2E A, SRC A and ST2E B, <br> SRC B) both ST2E cards will look at the remaining available input and light their appropri- <br> ate SRC lamps. If no inputs are available from either input card, the HOLDOVER lamp <br> lights steady green and the ACTIVE lamp lights green to indicate the ST2E cards are in free- <br> run mode. |  |

Chart 7. ST2E Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 5 | Before the ACTIVE lamps light on the ST2E cards, remove both clock input cards. <br> Requirements: The SRC lamps on the ST2E cards extinguish and the HOLDOVER lamps light steady green and the ACTIVE lamps will light on both ST2E cards to indicate they are in freerun mode. |
| 6 | Connect the multimeter, set to ohms, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and FREERUN (TB 11, pins 5 \& 6) <br> - CLOCK STATUS A RTN and LOCKED (TB11, pins 9 \& 10) <br> - CLOCK STATUS B RTN and FREERUN (TB12, pins $5 \& 6$ ) <br> - CLOCK STATUS B RTN and LOCKED (TB12, pins 9 \& 10) <br> Requirements: Both A and B FREERUN and LOCKED to RTN are closed ( $<15 \mathrm{ohms}$ ). <br> Note: Ohms cannot be measured across terminal sets if remote telemetry equipment is connected to the terminal sets. |
| 7 | Reinsert both clock input cards (CI/ACI). <br> Requirement: On the clock input cards, after the input source is acquired (approximately 8-40 seconds) the SRC ACT/SRC ACTIVE lamps light. The appropriate SRC lamps light, the HOLDOVER lamps go off, and the ACTIVE lamp remains lit on both ST2E cards. |
| 8 | Connect the multimeter, set to ohms, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and FREERUN (TB11, pins 5 \& 6) <br> - CLOCK STATUS B RTN and FREERUN (TB12, pins 5 \& 6) <br> Requirements: Both A and B FREERUN to RTN are open (infinite ohms). <br> Note: Ohms cannot be measured across terminal sets if remote telemetry equipment is connected to the terminal sets. |
| 9 | Up to 5 minutes later, observe the LKD lamps. <br> Requirement: On both ST2E cards, the LKD lamps light green. <br> Note: This indicates that the ST2E cards are frequency locked with their input sources. |

Chart 7. ST2E Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 10 | Connect the multimeter, set to ohms, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and LOCKED (TB11, pins 9 \& 10) <br> - CLOCK STATUS B RTN and LOCK (TB11, pins 9 \& 10) <br> Requirement: Both A and B LOCK to RTN are open (infinite ohms). <br> Note: Ohms cannot be measured across terminal sets if remote telemetry equipment is con- <br> nected to the terminal sets. |
| 11 | Press the DSBL pushbutton on the ST2E A card. <br> Requirement: The LKD and ACTIVE lamps go off, and the HOLDOVER lamp flashes green. <br> The SRC A lamp remains lit. |
| 12 | Press the DSBL pushbutton on the ST2E B card. <br> Requirement: Nothing happens; the LKD, ACTIVE, and SRC B lamps remain lit. <br> Note: The DSBL pushbutton does not function on the ST2E card installed in slot B. |
| 13 | Up to 20 minutes later, observe the ST2E A card lamps. <br> Requirement: The HOLDOVER lamp stops flashing and goes off. Then the ACTIVE lamp <br> lights, and then the LKD lamp lights. |
| 14 | Press the DSBL pushbutton on the ST2E B card. <br> Requirement: No lamps change status. <br> Note: The DSBL pushbutton does not function on the ST2E card installed in slot B, even if <br> the ST2E card installed in slot A is LKD and ACTIVE. |
| 15 | Remove the clock input A card. <br> Requirement: The ACTIVE lamp on the ST2E A card remains lit, the lit SRC A lamp goes off <br> and the SRC B lamp lights to indicate that the card is now receiving input reference from clock <br> input B card. The LKD lamp may go off, if so, it will relight after approximately 5 minutes. |
| Reinsert the clock input A card. <br> Requirement: After the input source is acquired (approximately 8 - 40 seconds), the SRC ACT/ <br> SRC ATCTIVE lamp lights on the input A card, and the SRC A lamp lights and the SRC B lamps <br> goes off on the ST2E A card. The LKD lamp may go off, if so, it will relight after approximately <br> 5 minutes. |  |

Chart 7. ST2E Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 17 | Remove the clock input B card. <br> Requirement: The ACTIVE lamp on the ST2E B card remains lit, the lit SRC B lamp goes off, and the SRC A lamp lights to indicate that the card is now receiving input reference from the clock input A card. The LKD lamp may go off, if so, it will relight after approximately 5 minutes. |
| 18 | Reinsert the clock input B card. <br> Requirement: After the input source is acquired (approximately 8-40 seconds), the SRC ACT/ SRC ATCTIVE lamp lights on the clock input B card. The SRC B lamp lights and the SRC A lamp goes off on the ST2E B card. The LKD lamp may go off, if so, it will relight after approximately 5 minutes. |
| 19 | Remove both clock input cards. <br> Requirement: On both ST2E cards the SRC and LKD lamps go off, the ACTIVE lamps remain lit, and the HOLDOVER lamps light red to indicate they are in holdover mode. |
| 20 | Connect the multimeter, set to volts dc scale, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and HOLDOVER (TB11, pins $7 \& 8$ ) <br> - CLOCK STATUS B RTN and HOLDOVER (TB12, pins $7 \& 8$ ) <br> Requirements: Both A and B HOLDOVER to RTN read $<0.1$ volts dc. <br> Note: The HOLDOVER and RTN status terminal set is connected to -48 V and battery return through relay windings for the holdover MAJOR/MINOR option switch (SW11) operation. Therefore, ohms cannot be measured across this terminal set. |
| 21 | Reinsert both clock input cards. <br> Requirement: The SRC ACT/SRC ACTIVE lamps light on the clock input cards, the HOLDOVER lamps on both ST2E cards go off, SRC A on the ST2E A and the SRC B on the ST2E B lights; then the LKD lamps on the ST2E cards light. |
| 22 | Connect the multimeter, set to volts dc scale, across the following terminal sets on the shelf backplane: <br> - CLOCK STATUS A RTN and HOLDOVER (TB11, pins $7 \& 8$ ) <br> - CLOCK STATUS B RTN and HOLDOVER (TB12, pins 7 \& 8) <br> Requirements: Both A and B HOLDOVER to RTN read -48 volts dc. <br> Note: The HOLDOVER and RTN status terminal set is connected to -48 V and battery return through relay windings for the holdover MAJOR/MINOR option switch (SW11) operation. Therefore, ohms cannot be measured across this terminal set. |

Chart 7. ST2E Card Test (DCD-ST2 Master Shelf Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 23 | This procedure is completed. Indicate completion of the ST2E Card Test on the Test Sign-off form, <br> then proceed to next chart. |

## Chart 8. ST3E Card Test

STEP PROCEDURE

Use this procedure to verify the operation of the ST3E cards. If ST3E cards are not used, skip this chart. This procedure assumes the procedure for the CI/ACI cards has been completed.

| 1 | On the rear of the DCD-ST2 master shelf, set the ST3/ST2 switch (SW1) to the ST3 position and the HOLDOVER ALARM switch (SW11-1) to MIN (Figure 3a). If the master shelf is a DCD-400/ CIM shelf, skip this step. |
| :---: | :---: |
| 2 | On both ST3E cards, set SW1, section 5 to OFF to cause a minor alarm in Holdover mode per Figure 6. |
| 3 | In the master shelf, insert the first ST3E card into the ST A slot. |
| 4 | In the master shelf, insert the second ST3E card into the ST B slot. |
| 5 | During the 30-minute (approximately) oscillator stabilization period, observe the FREE RUN lamp on both ST3E cards. <br> Requirement: On both ST3E cards, the FREE RUN lamps flash green. |
| 6 | After the 30-minute oscillator stabilization period, observe the FREE RUN, REF A, and REF B lamps. <br> Requirement: On both ST3E cards, the FREE RUN lamps stop flashing and go off. On both ST3E cards, the REF A or B lamp lights, depending on which clock input (A or B) card's SRC ACT/SRC ACTIVE lamp is lit. <br> Note: If a ST3E card does not recognize an input reference signal, the ST3E free runs and the FREE RUN lamp lights green (not flashing). |
| 7 | Approximately 5 minutes (up to 20 minutes for worst-case signal conditions) after the end of the 30-minute oscillator stabilization period, observe the LOCKED and ACTIVE lamps on the ST3E cards. <br> Requirement: On both ST3E cards, the LOCKED and ACTIVE lamps light green. |
| 8 | Press the transfer (XFR) pushbutton on either clock input card. Observe the SRC ACT/SRC ACTIVE lamps on the clock input cards and the REF and LOCKED lamps on the ST3E cards. <br> Requirement: The SRC ACT/SRC ACTIVE lamp goes off on one clock input card and lights on the other. The lit REF lamp (REF A or REF B) goes off and the other REF lamp lights on both ST3E cards. If the clock input cards are off frequency from each other, the LOCKED lamps on the ST3E cards may go off while converging on the new reference and relight after approximately 5 minutes. |

## Chart 8. ST3E Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 9 | Press the transfer (XFR) pushbutton on either clock input card to transfer back to the original <br> clock input card. Observe the SRC ACT/SRC ACTIVE lamps on the clock input cards and the REF <br> and LOCKED lamps on the ST3E cards. <br> Requirement: The SRC ACT/SRC ACTIVE lamp goes off on one clock input card and lights on <br> the other. The lit REF lamp (REF A or REF B) goes off and the other REF lamp lights on both <br> ST3E cards. If the clock input cards are off frequency from each other, the LOCKED lamps on <br> the ST3E cards may go off while converging on the new reference and relight after approximately <br> 5 minutes. |
| 10 | If clock input A card SRC ACT/SRC ACTIVE lamp is not lit, press its XFR pushbutton to make it <br> active. Remove clock input A card. Observe the SRC ACT/SRC ACTIVE lamp on the clock input B <br> card and the REF and LOCKED lamps on the ST3E cards. <br> Requirement: SRC ACT/SRC ACTIVE lamp on clock input B card lights. The REF A lamp goes <br> off and the REF B lamps light on both ST3E cards. The LOCKED lamp on one or both ST3E cards <br> may go off while converging on the new reference and relight after approximately 5 minutes. |
| 11 | Reinsert clock input A card. When the FAIL lamp goes off on clock input A card, observe the REF <br> lamps (A and B) on both ST3E cards. <br> Requirement: No change on the lamps. |
| 12 | Remove both clock input A and B cards. Observe the lamps on the AI/FA and both ST3E cards. <br> Requirement: On the AI/FA card, the MINOR lamp lights. On both ST3E cards, the REF and <br> LOCKED lamps go off, the HOLD OV lamps light, and the ACTIVE lamps remain lit. |

Chart 8. ST3E Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 13 | Connect the multimeter across the following Office Alarms, Shelf Status, and Clock Status A and <br> B (DCD-ST2 shelf only) terminal sets on the shelf backplane (see Figure 3). Set the multimeter <br> to ohms scale for all terminal sets except HOLDOVER; set to volts dc scale for Clock Status A <br> and B HOLDOVER terminal sets. |
| Office Alarm (all shelf types): |  |
| MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ on all shelf types) <br> Shelf Status (all shelf types): |  |
| MINSI and MINSR (TB2, pins 7 \& 8 on all shelf types) <br> Clock Status A(DCD-ST2 shelf only): |  |
| RTN and HOLDOVER (TB11, pins 7 \& 8) <br> RTN and LOCKED (TB11, pins 9 \& 10) <br> Clock Status B ( (DCD-ST2 shelf only): |  |
| RTN and HOLDOVER (TB12, pins 7 \& 8) <br> RTN and LOCKED (TB12, pins 9 \& 10) |  |
| Requirement: All alarm contacts, except HOLDOVER, are closed (<15 ohms) and HOLDOVER <br> reads <0.1 volts. |  |
| Note: Ohms cannot be measured across terminal sets if office alarm system and remote te- <br> lemetry equipment are connected to the terminal sets. The HOLDOVER and RTN status ter- <br> minal set is connected to -48V and battery return through relay windings for the holdover <br> MAJOR/MINOR option switch (SW11-1) operation on DCD-ST2 shelves. Therefore, ohms <br> cannot be measured across this terminal set. |  |
| Reinsert both clock input cards. After the FAIL lamps go off (approximately 8 to 40 seconds), <br> observe the lamps on the AI/FA and both ST3E cards after about 5 minutes. <br> Requirement: On both ST3E cards, the REF A or B lamp is lit (depending on which clock input <br> [A or B] card's SRC ACT/SRC ACTIVE lamp is lit), the LOCKED and ACTIVE lamps are lit, and <br> the HOLD OV lamp is off. The MINOR lamp on the AI/FA is off. |  |

## Chart 8. ST3E Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 15 | Connect the multimeter across the following Office Alarms, Shelf Status, and Clock Status A and B DCD-ST2 shelf only) terminal sets on the shelf backplane (see Figure 3). Set the multimeter to ohms scale for all terminal sets except HOLDOVER; set to volts dc scale for Clock Status A and B HOLDOVER terminal sets. <br> Office Alarm (all shelf types): <br> MINOR AUD NO and C (TB2, pins $5 \& 6$ ) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ on all shelf types) <br> Shelf Status (all shelf types): <br> MINSI and MINSR (TB2, pins $7 \& 8$ on all shelf types) <br> Clock Status A (DCD-ST2 shelf only): <br> RTN and HOLDOVER (TB11, pins $7 \& 8$ ) <br> RTN and LOCKED (TB11, pins $9 \& 10$ ) <br> Clock Status B ( (DCD-ST2 shelf only): <br> RTN and HOLDOVER (TB12, pins $7 \& 8$ ) <br> RTN and LOCKED (TB12, pins 9 \& 10) <br> Requirement: All alarm contacts, except HOLDOVER, are open (infinite ohms), and HOLDOVER reads -48 volts. <br> Note: Ohms cannot be measured across terminal sets if office alarm system and remote telemetry equipment are connected to the terminal sets. The HOLDOVER and RTN status terminal set is connected to -48 V and battery return through relay windings for the holdover MAJOR/MINOR option switch (SW11-1) operation. Therefore, ohms cannot be measured across this terminal set. |
| 16 | Remove both ST3E cards. On both ST3E cards, set SW1, section 5 to cause a major alarm in Holdover mode per Figure 6. On the DCD-ST2 shelf only, set HOLDOVER ALARM switch SW11-1 to MAJ on the shelf backplane. |
| 17 | Reinsert both ST3E cards and wait approximately 35 minutes for the following requirements to be met. <br> Requirement: On both ST3E cards, the REF A or B lamp lights, depending on which clock input (A or B) card's SRC ACT/SRC ACTIVE lamp is lit, and the LOCKED and ACTIVE lamps are lit green. |
| 18 | Remove both clock input cards. Observe the lamps on the AI/FA and both ST3E cards. <br> Requirement: The MAJOR and MINOR lamps light on the AI/FA card. On both ST3E cards, the REF and LOCKED lamps go off, and the HOLD OV lamp lights. |

Chart 8. ST3E Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 19 | Connect the multimeter across the following Office Alarms, Shelf Status, and Clock Status A and B DCD-ST2 shelf only) terminal sets on the shelf backplane (see Figure 3). Set the multimeter to ohms scale for all terminal sets except HOLDOVER; set to volts dc scale for Clock Status A and B HOLDOVER terminal sets. <br> Office Alarm (on all shelf types): <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins 2 \& 3) <br> Shelf Status (on all shelf types: <br> MINSI and MINSR (TB2, pins $7 \& 8$ ) <br> MAJSI and MAJSR (TB3, pins 7 \& 8) <br> Clock Status A (on DCD-ST2 shelf only): <br> RTN and HOLDOVER (TB11, pins 7 \& 8) <br> RTN and LOCKED (TB11, pins 9 \& 10) <br> Clock Status B (on DCD-ST2 shelf only): <br> RTN and HOLDOVER (TB12, pins 7 \& 8) <br> RTN and LOCKED (TB12, pins 9 \& 10) <br> Requirement: All alarm contacts, except HOLDOVER, are closed (<15 ohms); HOLDOVER reads $<0.1$ volts. <br> Note: Ohms cannot be measured across terminal sets if office alarm system and remote telemetry equipment are connected to the terminal sets. The HOLDOVER and RTN status terminal set is connected to -48 V and battery return through relay windings for the holdover MAJOR/MINOR option switch (SW11-1) operation on DCD-ST2 shelves. Therefore, ohms cannot be measured across this terminal set. |
| 20 | Reinsert both clock input cards. After the FAIL lamps go off, observe the lamps on the AI/FA and both ST3E cards after approximately 5 minutes. <br> Requirement: On both ST3E cards, the REF A or B lamp is lit (depending on which clock input card's SRC ACT/SRC ACTIVE lamp is lit), the LOCKED and ACTIVE lamps are lit, and the HOLD OV lamp is off. The MAJOR and MINOR lamps on the AI/FA card are off. |

## Chart 8. ST3E Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 21 | Connect the multimeter across the following Office Alarms, Shelf Status, and Clock Status A and B terminal sets on the shelf backplane (see Figure 3). Set the multimeter to ohms scale for all terminal sets except HOLDOVER; set to volts dc scale for Clock Status A and B HOLDOVER terminal sets. <br> Office Alarm (on all shelf types): <br> MINOR AUD NO and C (TB2, pins 5 \& 6) <br> MINOR VIS NO and C (TB2, pins $2 \& 3$ ) <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB3, pins $2 \& 3$ ) <br> Shelf Status (on all shelf types: <br> MINSI and MINSR (TB2, pins $7 \& 8$ ) <br> MAJSI and MAJSR (TB3, pins $7 \& 8$ ) <br> Clock Status A (on DCD-ST2 shelf only): <br> RTN and HOLDOVER (TB11, pins 7 \& 8) <br> RTN and LOCKED (TB11, pins 9 \& 10) <br> Clock Status B (on DCD-ST2 shelf only): <br> RTN and HOLDOVER (TB12, pins $7 \& 8$ ) <br> RTN and LOCKED (TB12, pins 9 \& 10) <br> Requirement: All alarm contacts, except HOLDOVER, are open (infinite ohms), and HOLDOVER reads -48 volts. <br> Note: Ohms cannot be measured across terminal sets if office alarm system and remote telemetry equipment are connected to the terminal sets. The HOLDOVER and RTN status terminal set (DCD-ST2 shelf only) is connected to -48 V and battery return through relay windings for the holdover MAJOR/MINOR option switch (SW11-1) operation. Therefore, ohms cannot be measured across this terminal set. |
| 22 | Consult the local company Installation Job Specifications as to whether the Holdover mode is to generate a major or minor alarm. If minor, remove both ST3E cards, set SW1, section 5, to MINOR and reinsert the cards. Set the HOLDOVER ALARM switch (SW11-1) to MIN on the DCD-ST2 shelf backplane. If it is to be set for major alarm, proceed to the next step. <br> Note: Both ST3E cards and SW3 on the DCD-ST2 shelf backplane must be set for the same mode, either MAJOR or MINOR alarm, in Holdover mode. The DCD-400/CIM shelf does not have a HOLDOVER switch on its backplane. |
| 23 | This procedure is completed. Indicate completion of the ST3E Card Test on the Test Sign-off form, then proceed to next chart. |



SW1 Switch Settings

| Section <br> (Note 1) | Setting | Function | Factory <br> Setting |
| :---: | :---: | :--- | :---: |
| 3 <br> (Note 2) | ON | $\pm 2 \times 10^{-6}$ pull-in | - |
|  | OFF | $\pm 5.6 \times 10^{-6}$ pull-in | X |
| 5 |  |  |  |
|  | ON | Holdover causes a major alarm (Note 5) | - |
|  | OFF | Holdover causes a minor alarm (Note 5) | X |

Notes:

1. All sections of switch SW1 not listed must be set to the OFF position.
2. If the clock input reference source to the DCD shelf is ST3E quality or better, switch section 3 may be set to ON. If not, it must be set to OFF.
3. Two versions of the ST3E card exist;

- p/n 090-40019-01 Issue C or later, setting section 5 of SW1 determines whether holdover activates a major or minor alarm.
- p/n 090-40019-03 Issue A, section 5 of SW1 is not functional, and holdover always causes a major alarm.

4. Both ST3E cards and HOLDOVER ALARM switch (SW11, position 1) on the DCD-ST2 shelf backplane must be set to the same mode, either MAJOR or MINOR alarm, in Holdover mode.
5. Loss of all input references causes the card to go into Holdover mode.

Figure 6. ST3E Card Switch

## Chart 9. ST3 Card Test

STEP PROCEDURE

Use this procedure to verify the operation of the ST3 cards. If ST3 cards are not used, skip this chart. This procedure assumes the procedure for the CI/ACI cards has been completed. The ST3 card is completely configured at the factory and no switch or jumper settings to the card are required.
$\left.\begin{array}{|c|l|}\hline 1 & \begin{array}{l}\text { On the rear of the DCD-ST2 master shelf, set the ST3/ST2 switch (SW1) to the ST3 position. The } \\ \text { HOLDOVER ALARM switch (SW11-1) on the DCD-ST2 backplane does not affect shelf operation } \\ \text { when ST3 cards are installed. Neither the DCD-400 nor the DCD-CIM have these two switches. }\end{array} \\ \hline 2 & \text { In the master shelf, insert the first ST3 card into the ST A slot. } \\ \hline 3 & \text { In the master shelf, insert the second ST3 card into the ST B slot. } \\ \hline 4 & \begin{array}{l}\text { Observe the FAIL and LOCK lamps on both ST3 cards. } \\ \text { Requirement: On both ST3 cards, the FAIL lamps go off. After approximately 40 seconds, the } \\ \text { LOCK lamps go off, and either REF A or B lamp is lit, depending on which clock input (A or B) } \\ \text { card's SRC ACT/SCR ACTIVE lamp is lit. }\end{array} \\ \hline 5 & \begin{array}{l}\text { Press the transfer (XFR) pushbutton on either clock input card. Observe the SRC ACT/SRC } \\ \text { ACTIVE lamps on the clock input cards and the REF and LOCK lamps on the ST3 cards. } \\ \text { Requirement: The SRC ACT/SRC ACTIVE lamp goes off on one clock input card and lights on } \\ \text { the other. The lit REF lamp (REF A or REF B) goes off and the other REF lamp lights on both } \\ \text { ST3 cards. If the clock input cards are off frequency from each other, the LOCK lamps on the ST3 } \\ \text { cards may light momentarily. }\end{array} \\ \hline 6 & \begin{array}{l}\text { Press the transfer (XFR) pushbutton on either clock input card to transfer back to the original } \\ \text { clock input card. Observe the SRC ACT/SRC ACTIVE lamps on the clock input cards and the REF } \\ \text { and LOCK lamps on the ST3 cards. }\end{array} \\ \hline 7 & \begin{array}{l}\text { Requirement: The SRC ACT/SRC ACTIVE lamp goes off on one clock input card and lights on } \\ \text { the other. The lit REF lamp (REF A or REF B) goes off and the other REF lamp lights on both } \\ \text { ST3 cards. If the clock input cards are off frequency from each other, the LOCK lamps on the ST3 } \\ \text { cards may light momentarily. }\end{array} \\ \hline \begin{array}{l}\text { If clock input A card SRC ACT/SRC ACTIVE lamp is not lit, then press its XFR pushbutton to } \\ \text { make it active. Remove clock input A card. Observe the SRC ACT/SRC ACTIVE lamp on the clock } \\ \text { input B card and the REF and LOCK lamps on the ST3 cards. }\end{array} \\ \begin{array}{l}\text { Requirement: SRC ACT/SRC ACTIVE lamp on clock input B card lights. The REF A lamp goes } \\ \text { off and the REF B lamp lights on both ST3 cards. The LOCKED lamp on one or both ST3 cards } \\ \text { may light momentarily. }\end{array} \\ \text { Caution: DO NOT perform this test on in-service DCD shelves because it will ad- } \\ \text { versely affect network elements receiving timing from the shelf. }\end{array}\right\}$

## Chart 9. ST3 Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 8 | $\begin{array}{l}\text { Reinsert clock input A card. When the FAIL lamp goes off on clock input A card, observe the REF } \\ \text { lamps (A and B) on both ST3 cards. } \\ \text { Requirement: No change on the lamps. }\end{array}$ |
| 9 | $\begin{array}{l}\text { Remove both clock input cards. Observe the lamps on the AI/FA and both ST3 cards. } \\ \text { Requirement: The MAJOR and MINOR lamps light on the AI/FA card. On both ST3 cards, the } \\ \text { REF lamps go off, and the FAIL and LOCK lamps light red. } \\ \text { Caution: DO NOT perform this test on in-service DCD shelves because it will ad- } \\ \text { versely affect network elements receiving timing from the shelf. }\end{array}$ |
| 10 | $\begin{array}{l}\text { Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- } \\ \text { nal sets on the shelf backplane (see Figure 3c): } \\ \text { Office Alarm: }\end{array}$ |
| $\begin{array}{l}\text { MINOR AUD NO and C (TB2, pins } 5 \text { \& 6) } \\ \text { MINOR VIS NO and C (TB2, pins 2 \& 3) } \\ \text { MAJOR AUD NO and C (TB3, pins } 5 \text { \& 6) } \\ \text { MAJOR VIS NO and C (TB2, pins } 2 \text { \& 3) }\end{array}$ |  |
| Shelf Status: |  |
| MINSI and MINSR (TB2, pins 7 \& 8) |  |
| MAJSI and MAJSR (TB3, pins 7 \& 8) |  |
| Requirement: All alarm contacts are closed (<15 ohms). |  |
| Note: Ohms cannot be measured across terminal sets if office alarm system and remote te- |  |
| lemetry equipment are connected to the terminal sets. |  |\(\left.\} \begin{array}{l}Reinsert both clock input cards. After the FAIL lamps go off, observe the lamps on the AI/FA and <br>

both ST3 cards. <br>
Requirement: On both ST3 cards, the REF A or B lamp is lit (depending on which clock input <br>
card's SRC ACT/SRC ACTIVE lamp is lit), and the LOCK and FAIL lamps are off. The MAJOR <br>
and MINOR lamps on the AI/FA card are off.\end{array}\right\}\)

## Chart 9. ST3 Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 12 | Connect the multimeter, set to ohms, across the following Office Alarm and Shelf Status termi- <br> nal sets on the shelf backplane (see Figure 3c): <br> Office Alarm: |
| MINOR AUD NO and C (TB2, pins $5 \& 6$ ) <br> MINOR VIS NO and C (TB2, pins $2 \& 3)$ <br> MAJOR AUD NO and C (TB3, pins 5 \& 6) <br> MAJOR VIS NO and C (TB2, pins 2 \& 3) |  |
| $\underline{$ Shelf Status:  <br>  MINSI and MINSR (TB2, pins 7 \& 8)  <br>  MAJSI and MAJSR (TB3, pins 7 \& 8) $}$Requirement: All alarm contacts are open (infinite ohms). <br> Note: Ohms cannot be measured across terminal sets if office alarm system and remote te- <br> lemetry equipment are connected to the terminal sets. |  |
| 13 | This procedure is completed. Indicate completion of the ST3 Card Test on the Test Sign-off form, <br> then proceed to next chart. |

## Chart 10. MCA/MCA-2 Card Test (DCD-ST2/ 400 Master and Expansion Shelves Only)

## STEP

## PROCEDURE

Use this procedure to verify the operation of the MCA/MCA-2 card. If the MCA/MCA-2 card is not required, skip to the next chart. This procedure assumes that installation of the ST2, ST2E, ST3E, or ST3 cards has been completed. The MCA/MCA-2 card is not required for a DCD-CIM Master or Expansion Shelf.

An MCA/MCA-2 card is not required if unprotected outputs are desired. If this condition applies to the system being setup, proceed to the next chart.


Requirement: The AUTO lamp lights after all lamps momentarily light and go off.
3 Obtain two TOxA cards of the same type (i.e., two TOTA card), for each shelf installed. Set the two left sections of SW1 on each card (sections 1 and 2 on some TOxA cards and sections 4 and 3 on other TOxA cards) to different settings (i.e., on TOTA cards, set one card for D4 framing, and the other card for ESF framing).

4
Insert one of the TOxA cards into slot TO1 of each master and expansion shelf. Insert the other card into the HS TOC slot of each shelf installed.

Requirement: The ST, INPUT and OPTION (i.e., D4 or ESF on TOTA cards) lamps are lit green on both card on all shelves.

Note: The cards should have different OPTION lamps, i.e., D4 is lit on the TO1 card, and ESF is lit on the HS TOC card.

Chart 10. MCA/MCA-2 Card Test (DCD-ST2/400 Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 5 | $\begin{array}{l}\text { Setup a dual trace 100 MHz oscilloscope using the manufacturer's manual and Table C. Clip } \\ \text { the scope probes' ground leads together. Connect an appropriate value Test Load Impedance } \\ \text { resistor (see Figure 9 through Figure 12 for the type of TOxA card used) across the two scope } \\ \text { probes. At the output panel, connect the scope probes across each of the TO1 card's outputs } \\ \text { (refer to Table D and Table E for output terminals). Leave the scope probes connected to the } \\ \text { last output port checked. } \\ \text { Requirement: The output waveforms must be as shown in the waveform diagrams (Figure 9 } \\ \text { through Figure 12, depending on the card type used). }\end{array}$ |
| 6 | $\begin{array}{l}\text { Remove the TOxA card in slot TO1 to activate an automatic protection switch to the HS TOC } \\ \text { card. Observe the lamps on the AI/FA, MCA/MCA-2, HS TOC TOxA cards, and the Output Pro- } \\ \text { tection/Spare Select Panel. Also observe the oscilloscope waveform. }\end{array}$ |
| $\begin{array}{l}\text { Requirements: } \\ \text { - AI/FA card: MAJOR and MINOR lamps are off } \\ \text { - MCA/MCA-2 card: AUTO lamp flashes for 6 seconds and stays lit } \\ \text { - HS TOC TOxA card: Lit OPTION lamp goes off and the OPTION lamp that was lit on the } \\ \text { removed TO1 TOxA card lights on the HS TOC TOxA card }\end{array}$ |  |
| - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO1 and HS TOC are lit |  |
| - Oscilloscope: The waveform on the scope returns when switch operation is completed (scope |  |
| connection was made in Step 5) |  |$\}$

Chart 10. MCA/MCA-2 Card Test (DCD-ST2/400 Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 8 | $\begin{array}{l}\text { Simultaneously press and hold for 3 seconds, the pushbuttons over slots TO1 and HS TOC (on } \\ \text { the Output Protection/Spare Select Panel) to activate a manual protection switch to the HS TOC } \\ \text { card. Observe the lamps on the AI/FA, MCA/MCA-2, HS TOC TOxA cards, and the Output Pro- } \\ \text { tection/Spare Select Panel. Also observe the oscilloscope waveform. } \\ \text { Requirements: } \\ \text { - AI/FA card: MAJOR, and MINOR lamps are off } \\ \text { - MCA/MCA-2 card: MAN lamp flashes continuously for as long as the protection switch is ac- } \\ \text { tivated }\end{array}$ |
| - HS TOC TOxA card: No change to OPTION lamps |  |
| - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO1 and HS TOC are lit |  |
| - Oscilloscope: The waveform on the scope returns when switch operation is completed (scope |  |
| connection was made in Step 5) |  |$\}$

## Chart 10. MCA/MCA-2 Card Test (DCD-ST2/400 Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 14 | Remove the TOxA card in slot TO10 (TO4 for DCD-ST2 shelf) to activate an automatic protec- <br> tion switch to slot HS TOT. Observe the lamps on the AI/FA, MCA-2, HS TOT TOxA cards, and <br> the Output Protection/Spare Select Panel. Also observe the oscilloscope waveform. <br> Requirements: <br> - AI/FA card: MAJOR lamp lights <br> - MCA-2 card: AUTO lamp flashes for 6 seconds and remains lit <br> - HS TOT TOxA card: Lit OPTION lamp goes off and the OPTION lamp that was lit on the <br> removed TO10 (TO4 for DCD-ST2) TOxA card lights on the HS TOT TOxA card |
| - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO10 (TO4 for DCD- |  |
| ST2 shelf) and HS TOT are lit |  |

Chart 10. MCA/MCA-2 Card Test (DCD-ST2/400 Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 16 | $\begin{array}{l}\text { Simultaneously press and hold for 3 seconds, the pushbuttons over slots TO10 (TO4 for DCD- } \\ \text { ST2 shelf) and HS TOT (on the Output Protection/Spare Select Panel) to activate a manual pro- } \\ \text { tection switch to the HS TOT card. Observe the lamps on the AI/FA, MCA-2, HS TOT and TOxA } \\ \text { cards, and the Output Protection/Spare Select Panel. Also observe the oscilloscope waveform. } \\ \text { Requirements: } \\ \text { - AI/FA card: MAJOR lamp lights } \\ \text { - MCA-2 card: MAN lamp flashes continuously for as long as the protection switch is activat- } \\ \text { ed }\end{array}$ |
| - HS TOT TOxA card: No change to OPTION lamps |  |
| - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO10 (TO4 for DCD-ST2 |  |
| shelf) and HS TOT are lit |  |$\}$

## Chart 10. MCA/MCA-2 Card Test (DCD-ST2/400 Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 19 | $\begin{array}{l}\text { Remove the TOxA card in slot TO10 (TO4 for DCD-ST2 shelf) to activate an automatic protec- } \\ \text { tion switch to slot HS TOT. Observe the lamps on the AI/FA, MCA-2, HS TOT TOxA cards, and } \\ \text { the Output Protection/Spare Select Panel. Also observe the oscilloscope waveform. } \\ \text { Requirements: } \\ \text { - AI/FA card: MINOR lamp lights } \\ \text { - MCA-2 card: AUTO lamp flashes for } 6 \text { seconds and stays lit } \\ \text { - HS TOT TOxA card: No change to OPTION lamps }\end{array}$ |
| 20 | $\begin{array}{l}\text { - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO10 (TO4 for DCD-ST2 } \\ \text { shelf) and HS TOT are lit }\end{array}$ |
| - Oscilloscope: The waveform on the scope returns when switch operation is completed (scope |  |
| connection was made in Step 12) |  |\(\left.\left.\} \begin{array}{l}Reinsert the TOxA card in slot TO10 (TO4 for DCD-ST2 shelf). Wait 10 seconds, then press and <br>

hold for 3 seconds, the pushbutton over slot HS TOT (on the Output Protection/Spare Select <br>
cands, to release the protection swith. Observe the lamps ont the AI/FA, MCA-2, HS TOT TOxA Protection/Spare Select Panel. Also observe the oscilloscope waveform.\end{array}\right\} $$
\begin{array}{l}\text { Requirements: } \\
\text { - AI/FA card: MINOR lamp goes off } \\
\text { - MCA-2 card: MAN lamp flashes for } 6 \text { seconds and goes off } \\
\text { - HS TOT TOxA card: No change to OPTION lamps } \\
\text { - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO10 (TO4 for DCD-ST2 } \\
\text { shelf) and HS TOT go off } \\
\text { - Oscilloscope: The waveform on the scope returns when switch operation is completed (scope } \\
\text { connection was made in Step 12) }\end{array}
$$\right\}\)

Chart 10. MCA/MCA-2 Card Test (DCD-ST2/400 Master and Expansion Shelves Only) (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 21 | $\begin{array}{l}\text { Simultaneously press the pushbuttons over slots TO10 (TO4 for DCD-ST2 shelf) and HS TOT } \\ \text { (on the Output Protection/Spare Select Panel) and hold for 3 seconds to activate a manual pro- } \\ \text { tection switch to the HS TOT card. Observe the lamps on the AI/FA, MCA-2, HS TOT TOxA } \\ \text { cards, and the Output Protection/Spare Select Panel. Also observe the oscilloscope waveform. } \\ \text { Requirements: } \\ \text { - AI/FA card: MINOR lamp lights } \\ \text { - MCA-2 card: MAN lamp flashes continuously for as long as the protection switch is activat- } \\ \text { ed }\end{array}$ |
| - HS TOT TOxA card: No change to OPTION lamps |  |
| - Output Protection/Spare Select Panel: Pushbutton lamps over slots TO10 (TO4 for DCD-ST2 |  |
| shelf) and HS TOT are lit |  |
| - Oscilloscope: The waveform on the scope returns when switch operation is completed (scope |  |
| connection was made in Step 12) |  |$\}$



MCA Switch Settings

| Section | Setting | Function | Factory Setting |
| :---: | :---: | :---: | :---: |
| SW1 |  |  |  |
| MSTR | ON | Master mode of operation (used with SW2) | X |
| SLAVE | OFF | Not allowed (see Note) |  |
| AUTO | ON | Auto switching mode | X |
| MAN | OFF | Manual switching mode |  |
| OPT 1 | ON | Future use |  |
| * | OFF | Normal | X |
| TEST | ON | Factory use only |  |
| * | OFF | Normal | X |
| SW2 |  |  |  |
| MASTER | ON | Master mode of operation (used with SW1) | X |
| SLAVE | OFF | Not allowed (see Note) |  |

Note: SLAVE mode must not be used. Always set SW1 and SW2 to MSTR and MASTER.

Figure 7. MCA Card Switches


| Section | Setting | Function | Factory Setting |
| :---: | :---: | :---: | :---: |
| 1 | ON | Master mode of operation | X |
|  | OFF | Not allowed (see Note) | - |
| 2 | ON | Auto switching mode | X |
|  | OFF | Manual switching mode | - |
| 3 | ON | Minor alarm will be generated when protection switching occurs. If this is set to ON, section 5 must be set to OFF. | X |
|  | OFF | Minor alarm disabled | - |
| 4 | ON | Not allowed | - |
|  | OFF | Normal | X |
| 5 | ON | Major alarm will be generated when protection switching occurs. If this is set to ON, section 3 must be set to OFF. | - |
|  | OFF | Major alarm disabled | X |
| 6 | - | Not Used | - |
| 7 | - | Not Used | - |
| 8 | - | Not Used | - |

Note: SLAVE mode must not be used. Always set SW1 section 1 to Master.

Figure 8. MCA-2 Card Switch

Table D. DB9 Output Connections (MMP Module)


Table E. Wire-Wrap Output Connections (MMP Module or Wire-Wrap Panel)

| WIRE-WRAP LEADS * |  | TOTA, TOCA (DS1 or CC) |  | TOLA (RS-422)** |  | TOLA (TTL)*** |  | TOAA (ANALOG) |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OUTPUT | PIN | TIMING PORT | LEAD | TIMING PORT | LEAD | TIMING PORT | LEAD | TIMING PORT | LEAD |
| 1 | T $R$ S | 1 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 1 | $\begin{aligned} & \mathrm{D}+ \\ & \mathrm{D}- \end{aligned}$ | $\begin{aligned} & 2 \\ & 1 \end{aligned}$ | $\begin{aligned} & \text { D2 } \\ & \text { D1 } \end{aligned}$ |  |  |
| 2 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 2 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ |  |  | - | $\begin{aligned} & \mathrm{C} 2 \\ & \mathrm{C} 1 \end{aligned}$ |  |  |
| 3 | T $R$ S | 3 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 2 | $\begin{aligned} & \text { D+ } \\ & \text { D- } \end{aligned}$ | $\begin{aligned} & 4 \\ & 3 \end{aligned}$ | $\begin{aligned} & \text { D4 } \\ & \text { D3 } \end{aligned}$ | $2$ <br> (Note 1) | $\begin{aligned} & \mathrm{T} \\ & \mathrm{~S} \\ & \hline \end{aligned}$ |
| 4 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 4 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ |  |  | - | $\begin{aligned} & \text { C4 } \\ & \text { C3 } \end{aligned}$ |  |  |
| 5 | T R S | 5 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 3 | $\begin{aligned} & \text { D+ } \\ & \text { D- } \end{aligned}$ | $\begin{aligned} & 6 \\ & 5 \end{aligned}$ | $\begin{aligned} & \text { D6 } \\ & \text { D5 } \end{aligned}$ |  |  |
| 6 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 6 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ |  |  | - | $\begin{aligned} & \mathrm{C} 6 \\ & \mathrm{C} 5 \end{aligned}$ |  |  |
| 7 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 7 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 4 | $\begin{aligned} & \text { D+ } \\ & \text { D- } \end{aligned}$ | $\begin{aligned} & 8 \\ & 7 \end{aligned}$ | $\begin{aligned} & \text { D8 } \\ & \text { D7 } \end{aligned}$ |  |  |
| 8 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 8 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ |  |  | - | $\begin{aligned} & \mathrm{C} 8 \\ & \mathrm{C} 7 \end{aligned}$ | (Note 1) | $\begin{aligned} & \mathrm{T} \\ & \mathrm{~S} \\ & \hline \end{aligned}$ |
| 9 | T R S | 9 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 5 | $\begin{aligned} & \mathrm{D}+ \\ & \mathrm{D}- \end{aligned}$ | $\begin{gathered} 10 \\ 9 \\ - \end{gathered}$ | $\begin{gathered} \text { D10 } \\ \text { D9 } \end{gathered}$ |  |  |
| 10 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 10 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ |  |  | - | $\begin{gathered} \text { TP1 } \\ \text { C9 and C10 } \end{gathered}$ |  |  |

Legend: T=Tip C=Digital Ground $\quad \mathrm{S}=$ Shield Ground (Note 2) $\quad \mathrm{D}+=$ Data, $+422 \quad \mathrm{R}=$ Ring $\quad \mathrm{D}=$ Data, TTL $\mathrm{D}-=$ Data, -422
In the wire-wrap leads column, the paired outputs of 1 and 2,3 and 4,5 and 6,7 and 8,9 and 10 , use the $\mathrm{Tip}(\mathrm{T})$ of the oddnumbered output and the Tip of even-numbered output as one TTL output. The paired outputs of 1 and 2,3 and 4,5 and 6, 7 and 8, 9 and 10, use the Ring of the odd-numbered output and the Ring of even-numbered output as the other TTL output. When using TOLA RS-422, connect the output cable T and R leads to the odd-numbered wire-wrap T and R leads, respectively. The T lead connects internally to the TOLA RS-422 driver $D+$ and the $R$ lead to the driver $D$ - lead. The RS422 output is across the $T\left(D_{+}\right)$and $R\left(D_{-}\right)$leads.
For TOLA TTL operation, two outputs are derived from each RS-422 driver. One output across T (Dn) and T (Cn) leads, and the other outputs is across $\mathrm{R}(\mathrm{D}-)$ and $\mathrm{R}(\mathrm{Cn})$ leads (e.g., D2 and C2 are output 2, D1 and C1 are output 1).

1. When using TOAA, connect the shield lead of the coax cable to the Ring ( $R$ ) pin of the wire-wrap panel.
2. It is preferable to ground the shield at the timing source end only, but certain vendor applications may require grounding at the receive end. However, under no circumstances should the shield be connected to frame ground at both ends unless one end is capacitor coupled to ground.

Table F. SCIU Wire-Wrap Connections (MMP Module or Wire-Wrap Panel)

| WIRE-WRAP LEADS |  | CONNECTION | LEAD | WIRE-WRAP LEADS |  | CONNECTION | LEAD |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OUTPUT | PIN |  |  | OUTPUT | PIN |  |  |
| 1 | $\begin{aligned} & \hline T \\ & \hline R \\ & S \end{aligned}$ | B OUT | $\begin{aligned} & \hline T \\ & \hline R \\ & S \end{aligned}$ | 6 | $\begin{aligned} & \hline T \\ & \hline R \\ & S \end{aligned}$ | UNIT FAIL | SI RTN |
| 2 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | A IN | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | 7 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | DS1B FAIL | SI RTN |
| 3 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | WARNING | SI RTN | 8 | $\begin{aligned} & \mathrm{T} \\ & \mathrm{R} \\ & \mathrm{~S} \end{aligned}$ | DS1A FAIL | SI RTN |
| 4 | $\begin{aligned} & \hline \text { T } \\ & \text { R } \\ & S \end{aligned}$ | SLIP | SI RTN | 9 | $\begin{aligned} & \hline \text { T } \\ & \text { R } \\ & S \end{aligned}$ | B IN | $\begin{aligned} & \hline \text { T } \\ & R \\ & R \\ & S \end{aligned}$ |
| 5 | $\begin{aligned} & \hline T \\ & R \\ & S \end{aligned}$ | - | - | 10 | $\begin{aligned} & \hline T \\ & R \\ & S \end{aligned}$ | A OUT | $\begin{aligned} & \hline T \\ & R \\ & S \end{aligned}$ |
| Legend: T=Tip C=Digital Ground $\quad \mathrm{S}=$ Shield Ground* $\quad$ D+=Data, $+422 \quad$ R=Ring $\quad$ D=Data, TTL D-=Data, -422 <br> * It is preferable to ground the shield at the timing source end only, but certain vendor applications may require grounding at the receive end. However, under no circumstances should the shield be connected to frame ground at both ends unless one end is capacitor coupled to ground. |  |  |  |  |  |  |  |


$\mathrm{t}=125 \mu \mathrm{~s}$ for 8 kHz $15.625 \mu \mathrm{~s}$ for 64 kHz $1.9531 \mu \mathrm{~s}$ for 512 kHz
$1.0 \mu \mathrm{~s}$ for 1.0 MHz
$0.48828 \mu \mathrm{~s}$ for 2.048 MHz
Test Load Impedance: 75 ohms for coax cable

Note: All values are nominal
Figure 9. TOAA Card Output Waveform


PULSE RISE TIME <500 NS
Test Load Impedance: 133 ohms for 22 AWG twisted pair cable

Figure 10. TOCA Card Output Waveform


Test Load Impedance: 100 ohms for 22 AWG twisted pair cable. 75 ohms for coax cable
Note: only the positive pulse is shown. The negative pulse is an upside-dow mirror image of the positive pulse.

Figure 11. TOTA Card Output Waveform
$\mathrm{t}_{\mathrm{B}}=$ Bit time duration ( $4 \mathrm{~kb} / \mathrm{s}$ to $1.544 \mathrm{Mb} / \mathrm{s}$ )
$t_{R} \leq t_{B} / 10$
$\mathrm{V}_{\mathrm{SS}}=$ Difference in steady-state voltages:
2 to 6 V pp for RS-422 (balanced) into $100 \Omega$
$>3 \mathrm{~V}$ for RS-423 (unbalanced) into $450 \Omega$
$>+3 \mathrm{~V}$ from GND and $>-3 \mathrm{~V}$ from GND for RS-232 into 3 to $7 \mathrm{k} \Omega$


Bit Duration

| Data Rate | $\mathrm{t}_{\mathrm{B}}$ | $\mathrm{t}_{\mathrm{R}}$ |
| :---: | :---: | :---: |
| $4 \mathrm{~kb} / \mathrm{s}$ | $125 \mu \mathrm{~s}$ | $\leq 12.5 \mu \mathrm{~s}$ |
| $4.8 \mathrm{~kb} / \mathrm{s}$ | $104.1 \mu \mathrm{~s}$ | $\leq 10.41 \mu \mathrm{~s}$ |
| $8 \mathrm{~kb} / \mathrm{s}$ | $62.5 \mu \mathrm{~s}$ | $\leq 6.25 \mu \mathrm{~s}$ |
| $9.6 \mathrm{~kb} / \mathrm{s}$ | $52 \mu \mathrm{~s}$ | $\leq 5.2 \mu \mathrm{~s}$ |
| $19.2 \mathrm{~kb} / \mathrm{s}$ | $26 \mu \mathrm{~s}$ | $\leq 2.6 \mu \mathrm{~s}$ |
| $56 \mathrm{~kb} / \mathrm{s}$ | $8.9 \mu \mathrm{~s}$ | $\leq 890 \mathrm{~ns}$ |
| $64 \mathrm{~kb} / \mathrm{s}$ | $7.8 \mu \mathrm{~s}$ | $\leq 780 \mathrm{~ns}$ |
| $256 \mathrm{~kb} / \mathrm{s}$ | $1.95 \mu \mathrm{~s}$ | $\leq 195 \mathrm{~ns}$ |
| $384 \mathrm{~kb} / \mathrm{s}$ | $1.3 \mu \mathrm{~s}$ | $\leq 130 \mathrm{~ns}$ |
| $512 \mathrm{~kb} / \mathrm{s}$ | 977 ns | $\leq 97.7 \mathrm{~ns}$ |
| $768 \mathrm{~kb} / \mathrm{s}$ | 651 ns | $\leq 65.1 \mathrm{~ns}$ |
| $1.536 \mathrm{Mb} / \mathrm{s}$ | 326 ns | $\leq 32.6 \mathrm{~ns}$ |
| $1.544 \mathrm{Mb} / \mathrm{s}$ | 324 ns | $\leq 32.4 \mathrm{~ns}$ |

Test Load Impedance: 100 ohms for 22 AWG twisted pair cable

Figure 12. TOLA Card Output Waveform

## Chart 11. Timing Output Card Test

## STEP

## PROCEDURE

Use this procedure to verify the operation of the Timing Output (TOxA) cards. This procedure assumes the procedure for the MCA/MCA-2 card has been completed.

## Notes:

1. On TOCA and TOTA cards, all 10 outputs are identical.
2. Only 2 hot spare (HS) slots are available; therefore, only 2 types of TOxA cards can be protected in a single shelf. If all cards in the shelf are the same type (all TOCA, TOTA, TOAA-0x, or TOLA-0x), only one HS card (of the same type) is required. Refer to Table G for TOxA card type incompatibilities.
3. If an MCA/MCA-2 card is not installed, automatic protection switching is not available.
4. The timing output cabling to network elements must be disconnected during this test.

| 1 | Set the option switches on each TOxA card according to the local company Installation Job Spec- <br> fications. Refer to Figure 13 through Figure 16 for option settings. |
| :---: | :--- |
| 2 | Insert all TOxA cards in the TOx (x=1 through 10 [x=1 through 4 for the DCD-ST2 shelf]) slots in <br> all installed shelves (master and expansions) according to the local company Installation Job <br> Specifications. <br> Requirements: <br> - On the TOxA cards just installed, the FAIL and PORT ALM lamps are off. <br> Note: If the FAIL lamp lights, replace the card. <br> - The INPUT and ST lamps light green (indicating the cards are receiving reference signals <br> from the CI and ST cards). <br> Note: If the INPUT and ST lamps do not light, retest the CI/ACI and ST cards. <br> - The OPTION lamps are lit (i.e., D4 and ESF lamps on a TOTA card) per options selected <br> in Step 1. |
| 3 | Check each output of the cards just installed in the TOx slots at the output panel using a dual- <br> trace 100 MHz oscilloscope. Table D lists the output pins in the DB9 connector module, and <br> Table E lists the output signal pins when using a wire-wrap module or panel. Table C lists the <br> oscilloscope settings. The output waveforms must be as shown in the waveform diagrams (Fig- <br> ure 9 through Figure 12, depending on the card type used). The scope probes must be terminated <br> in the Test Load Impedance as stated in the waveform figures. |
| 4 | If the shelf is equipped with an MCA/MCA-2 card, install TOxA cards in the HS TOC and <br> HS TOT slots (these must be the same type as the TOxA cards to be protected in the TOxA slots) <br> according to the local company Installation Job Specifications. |

## Chart 11. Timing Output Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 5 | Activate and release a manual protection switch on each TOxA card installed, in all installed <br> shelves (master and expansions). The switch is activated by simultaneously pressing and hold- <br> ing for 3 seconds, the pushbuttons over the TOx slot and a like TOxA card in an HS slot. The <br> switch is released by pressing and holding for 3 seconds, the pushbutton over the HS slot. <br> Requirements: <br> • Upon activation, pushbutton lamps over the TOx and HS cards are lit. <br> - Upon release, pushbutton lamps over the TOx and HS cards go off. |
| 6 | Connect all the timing outputs per TMSL 097-40000-57, DCD Installation practice. |
| 7 | If SCIU cards are installed, proceed to the next chart, otherwise, testing of the DCD system is <br> completed. Complete the Test Sign-off form. |

Table G. TOXA Card Inc ompatibility

| CARD IN HS <br> SLOT | DO NOT INSTALL IN ANY TOx OR <br> HS SLOT IN THE SHELF | CARD IN HS <br> SLOT | DO NOT INSTALL IN ANY TOx OR <br> HS SLOT IN THE SHELF |
| :---: | :---: | :---: | :---: |
| TOAA-01 | TOAA-02, TOAA-03, TOLA-07 | TOLA-04 | TOLA-01, TOLA-02 |
| TOAA-02 | TOAA-01, TOAA-03, TOLA-07 | TOLA-05 | TOCA |
| TOAA-03 | TOAA-01, TOAA-02, TOLA-07 | TOLA-06 | TOTA |
| TOAA-05 | No restrictions | TOLA-07 | TOAA-01, TOAA-02, TOAA-03 |
| TOCA | TOLA-05 | TOGA | No restrictions |
| TOLA-01 | TOLA-02, TOLA-04 | TOTA | TOLA-06 |
| TOLA-02 | TOLA-01, TOLA-04 | TOEA | No restrictions |
| TOLA-03 | No restrictions |  |  |



SW1 Settings

| 1 | 2 | 3 | 4 | Meaning | Factory <br> Setting |
| :---: | :---: | :---: | :---: | :--- | :---: |
| ON | ON | - | - | 64 kHz or $8 \mathrm{kHz}^{*}$ | X |
| ON | OFF | - | - | 512 kHz | - |
| OFF | ON | - | - | 1.0 MHz | - |
| OFF | OFF | - | - | 2.048 MHz | - |
| - | - | ON | - | Not used | X |
| - | - | OFF | - | Not used | - |
| - | - | - | ON | Normal | X |
| - | - | - | OFF | Not allowed | - |

* 64 kHz for -01 and $-02 ; 8 \mathrm{kHz}$ for $-03 ; 5 \mathrm{MHz}$ for -05 .

Figure 13. TOAA Card Switch


Figure 14. TOCA Card Switch


Figure 15. TOTA Card Switch


Note: Refer to the specifications in TMSL 097-40000-55, General Description and Specifications for the output frequencies of each group.

Figure 16. TOLA Card Switch

## Chart 12. SCIU Card Test

## STEP

## PROCEDURE

Use this procedure to verify the operation of SCIU cards. This procedure assumes the installation has been completed per TMSL 097-40000-57, DCD Installation. If SCIU cards are not required, testing of the DCD is completed.

Equipment required: A DS1 Bit Error Rate Test Set (BERTS). This procedure assumes that standard level access jack sets (i.e., DSX-1) were cabled to the SCIU wire-wrap module or panel.

Note: If jack sets are not connected to the SCIU wire-wrap module, the BERTS will need to be clipped to the Network Element (NE) ends of the cables connected to the SCIU wire-wrap moduleor panel.

| 1 | Set up the BERTS transmit signal to match the framing format and line coding similar to the <br> traffic-carrying system in which the SCIU card will be inserted. |
| :---: | :--- |
| 2 | Set the option switches on each SCIU card as per the following (refer to Figure 17): <br> - Set SW1, sections 1 to 6 and 8 to 10 to OFF, and section 7 to ON. <br> - Set SW3, sections 1 to 5 for the cable length from the output module to the DSX for A direc- <br> tion of transmission and section 6 to 10 for the B direction. <br> - Set SW4, sections 1 and 2 to ON, and sections 3 and 4 to OFF. |
| 3 | Connect the 0 dB DSX (3 volts b-p) transmit signal from the BERTS to the SCIU jack set EAST <br> A IN jack with a patch cord. Connect another patch cord from the SCIU jack set WEST A OUT <br> jack to receive jack on the BERTS. Start the BERTS test and observe the receiver for 5 minutes. |
| 4 | Requirement: No DS1 parameters are exceeded (no errors) on the BERTS receiver. |
| Move the BERTS transmit patch cord from the EAST A IN jack to the WEST B IN jack. Move <br> the BERTS receive patch cord from the WEST A OUT jack to the EAST B OUT jack. Start the <br> BERTS test and observe the receiver for 5 minutes. <br> Requirement: No DS1 parameters are exceeded (no errors) on the BERTS receiver. |  |
| 5 | After the test, leave the BERTS patch cords up. |
| 6 | Insert an SCIU card in the first TOx slot, and check its lamp status. <br> Requirement: On the SCIU card just installed, the FAIL lamp lights and goes off, the SYNC <br> and DS1B lamps light green, and the DS1A lamp lights red. On the AI/FA card, the MAJOR <br> lamp is lit red to indicate that the DS1A receive signal is not present. <br> Note: If the FAIL lamp remains lit, replace the card. If the SYNC lamp is lit red, the card <br> is not receiving system reference from the clock input or clock cards, or expansion bus cable; <br> verify the clock input/clock cards are operating correctly in the master shelf. If in an expan- <br> sion shelf, verify the master to expansion cable is installed correctly. |

## Chart 12. SCIU Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 7 | Remove the SCIU card and change SW1, section 7 to OFF, and section 8 to ON. Reinsert the card. <br> Requirement: The MINOR lamp on the $\mathrm{AI} / \mathrm{FA}$ card is lit (MAJOR lamp goes off on $\mathrm{AI} / \mathrm{FA}$ card 6-10 seconds after the SCIU card is removed). |
| 8 | Start the BERTS test and observe the receiver for 5 minutes. <br> Requirement: No DS1 parameters are exceeded (no errors) on the BERTS receiver. |
| 9 | Move the BERTS transmit patch cord from the WEST B IN jack to the EAST A IN jack. Move the BERTS receive patch cord from the EAST B OUT jack to the WEST A OUT jack. Restart the BERTS test and observe the receiver for 5 minutes. <br> Requirement: No DS1 parameters are exceeded, except slips on the BERTS receiver. On the SCIU card, the DS1B lamp is lit red, the DS1A lamp is lit green, the bit slip lamps ( 0,64 , and 128) alternately light and go off, and the SLIP and HI SLIP lamps are lit red. The MAJOR and MINOR lamps on the AI/FA card are off. |
| 10 | Remove the SCIU card and change SW1, section 8 to OFF, and section 9 to ON. Reinsert the card. <br> Requirement: The MAJOR lamp is lit on the AI/FA card to indicate DS1B receive signal is not present. DS1B lamp is lit red on the SCIU card. |
| 11 | Remove the SCIU card and change SW1, section 9 to OFF, and section 10 to ON. Reinsert the card. <br> Requirement: The MINOR lamp is lit on the AI/FA card. DS1B lamp is lit red on the SCIU card (MAJOR lamp goes off on AI/FA card 6-10 seconds after the SCIU card is removed). |
| 12 | Remove the SCIU card and change SW1, section 10 to OFF, and section 3 to ON. Reinsert the card. <br> Requirement: Initially, the MAJOR and MINOR lamps are off on the AI/FA card. When the SLIP lamp on the SCIU card lights red (may take several minutes), the MAJOR lamp lights on the AI/FA card (MINOR lamp goes off on AI/FA card 6-10 seconds after the SCIU card is removed). |
| 13 | Remove the SCIU card and change SW1, section 3 to OFF, and section 4 to ON. Reinsert the card. <br> Requirement: When the SLIP lamp on the SCIU card lights red (may take several minutes), the MINOR lamp lights on the AI/FA card (MAJOR lamp goes off on AI/FA card 6-10 seconds after the SCIU card is removed). |

## Chart 12. SCIU Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 14 | $\begin{array}{l}\text { Remove the SCIU card and change SW1, section } 4 \text { to OFF, and section } 1 \text { to ON. Reinsert the } \\ \text { card. } \\ \text { Requirement: First the SLIP lamp on the SCIU card lights red after several minutes (the } \\ \text { MAJJOR and MINOR lamps on the AI/FA card are off), then after several more minutes, the } \\ \text { HI SLIP lamp on the SCIU card lights red, and the MAJOR lamp on the AI/FA card lights } \\ \text { (MINOR lamp goes off on AI/FA card 6-10 seconds after the SCIU card is removed). }\end{array}$ |
| 15 | $\begin{array}{l}\text { Remove the SCIU card and change SW1, section 1 to OFF, and section } 2 \text { to ON. Reinsert the } \\ \text { card. } \\ \text { Requirement: After several minutes the HI SLIP lamp on the SCIU card lights red, and the } \\ \text { MINOR lamp on the AI/FA card lights (MAJOR lamp goes off on AI/FA card 6-10 seconds af- } \\ \text { ter the SCIU card is removed). }\end{array}$ |
| 16 | $\begin{array}{l}\text { Remove the SCIU card and change SW1, section 2 to OFF. Reinsert the card and wait for the } \\ \text { SLIP and HI SLIP lamps to light (may take several minutes). } \\ \text { Requirement: The MAJOR and MINOR lamps on the AI/FA card are off. }\end{array}$ |
| 17 | $\begin{array}{l}\text { Connect a patch cord from a MON jack cabled to an NE that is being timed from the DCD } \\ \text { shelf, to the external clock reference jack on the BERTS. Restart the BERTS test and observe } \\ \text { the receiver for 5 minutes. }\end{array}$ |
| Requirement: No DS1 parameters are exceeded (no errors or slips) on the BERTS receiver. |  |$\}$

## Chart 12. SCIU Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :---: |
| 22 | Reinsert the BERTS transmit patch cord. <br> Requirement: BERTS receiver changes from framed all-ones back to BERTS transmit signal. |
| 23 | Send an out-of-frame (OOF) condition from the BERTS. <br> Requirement: BERTS receives a framed all-ones signal. |
| 24 | Restore framing from BERTS transmitter. <br> Requirement: BERTS receiver changes from framed all-ones back to BERTS transmit signal. |
| 25 | Remove SCIU card and set switch SW4, position 1 to OFF, and position 4 to ON and reinsert the card in the shelf. Restart the BERTS test and observe the receiver for 5 minutes. <br> Requirement: BERTS receiver sees same signal format as transmitted for the BERTS and no parameters are exceeded (no errors or slips). |
| 26 | Remove the transmit patch cord from the BERTS. <br> Requirement: BERTS receiver sees a loss-of-signal (LOS). |
| 27 | Reinsert the BERTS transmit patch cord. <br> Requirement: BERTS receiver sees the BERTS transmit signal. |
| 28 | Send OOF condition from the BERTS. <br> Requirement: BERTS receiver sees OOF. |
| 29 | Reinsert BERTS transmit patch cord. <br> Requirement: BERTS receiver sees the BERTS transmit signal. |
| 30 | On the SCIU card, insert a disabling pin (provided) in the BYPASS jack on the front panel. Restart the BERTS test. <br> Requirement: On the SCIU card, the BYPASS lamp is lit red. On a 5 minute BERTS test, no DS1 parameters are exceeded (no errors or slips). On the SCIU card, the SLIP, HI SLIP, and bit slip lamps are off. |
| 31 | Remove the patch cord from the BERTS external clock reference jack. Restart the BERTS test and observe the receiver for 5 minutes. <br> Requirement: No DS1 parameters are exceeded on the BERTS receiver. On the SCIU card, the SLIP, HI SLIP, and bit slip lamps are off. |

## Chart 12. SCIU Card Test (Contd)

| STEP | PROCEDURE |
| :---: | :--- |
| 32 | Remove the disabling pin from the BYPASS jack on the SCIU/ESCIU card. <br> Requirement: The BYPASS lamp goes off. |
| 33 | Remove the BERTS patch cords from the SCIU access jacks. |
| 34 | Remove the SCIU card and set option switches (SW1, SW3, and SW4) according to local com- <br> pany Installation Job Specifications (refer to Figure 17, Table H, and Table I). Reinsert card in <br> the shelf. |
| Note: SW1 (sections 5 and 6) WARN alarm option on the SCIU card must always be set to <br> OFF. The WARN alarm is associated with the 128 bit slip lamp on the front panel of the <br> SCIU card, and there is no way to reset or clear this alarm until a full frame slip occurs. |  |
| 35 | Repeat Steps 2 through 34 for each SCIU card to be installed in all shelves. |
| 36 | This procedure is completed. Indicate completion of the SCIU Card Test on the Test Sign-off <br> form. The acceptance testing of all DCD shelves and cards is completed. |



Notes:

1. SeeTable H for the SCIU card option switch settings.
2. Factory settings for SW1 and SW3 are OFF.
3. Factory settings for SW4: 0 / ALL 1 and ESF/D4 to ON, TERM/BRDG and OFF/OPT 1 to OFF.
4. WARN MAJ and MIN switch positions on SW1 must always be set to OFF.
5. On SW1, MAJ and MIN switch positions for each alarm (HI SLIP, SLIP, DS1A, and DS1B) are mutually exclusive - one ON and the other OFF - or both OFF for NO ALARM for that condition.

Figure 17. SCIU Card Switce hes

Table H. SCIU Option Settings

| SECTION | SETTING | RESULT |
| :---: | :---: | :---: |
| SWITCH SW1 (Note 1) |  |  |
| HI SLIP MAJ | ON | Major alarm |
| HI SLIP MIN | OFF |  |
| HI SLIP MAJ | OFF | Minor alarm |
| HI SLIP MIN | ON |  |
| HI SLIP MAJ | OFF | No alarm |
| HI SLIP MIN | OFF |  |
| SLIP MAJ | ON | Major alarm |
| SLIP MIN | OFF |  |
| SLIP MAJ | OFF | Minor alarm |
| SLIP MIN | ON |  |
| SLIP MAJ | OFF | No alarm |
| SLIP MIN | OFF |  |
| WARN MAJ | ON | Major alarm (See Note 2) |
| WARN MIN | OFF |  |
| WARN MAJ | OFF | Minor alarm (See Note 2) |
| WARN MIN | ON |  |
| WARN MAJ | OFF | No alarm (See Note 2) |
| WARN MIN | OFF |  |
| DS1A MAJ | ON | Major alarm |
| DS1A MIN | OFF |  |
| DS1A MAJ | OFF | Minor alarm |
| DS1A MIN | ON |  |
| DS1A MAJ | OFF | No alarm |
| DS1A MIN | OFF |  |
| DS1B MAJ | ON | Major alarm |
| DS1B MIN | OFF |  |
| DS1B MAJ | OFF | Minor alarm |
| DS1B MIN | ON |  |

Table H. SCIU Option Settings (Contd)

| SECTION | SETTING | RESULT |
| :---: | :---: | :---: |
| DS1B MAJ | OFF | No alarm |
| DS1B MIN | OFF |  |
| SWITCH SW3 (Note 3) |  |  |
| LBO A - 130 | ON | 0-130' |
| LBO A - 260 | ON | 130-260' |
| LBO A - 400 | ON | 260-400' |
| LBO A - 530 | ON | 400-530' |
| LBO A - 650 | ON | 530-650' |
| LBO B - 130 | ON | 0-130' |
| LBO B - 260 | ON | 130-260' |
| LBO B - 400 | ON | 260-400' |
| LBO B - 530 | ON | 400-530' |
| LBO B - 650 | ON | 530-650' |
| SWITCH SW4 |  |  |
| 0 / ALL 1 | See Table I |  |
| ESF / D4 | ON | D4 (SF)Framing |
|  | OFF | ESF Framing |
| TERM / BRDG (See Note 4) | ON | Bridging mode |
|  | OFF | Terminated mode |
| OFF / OPT1 | See Table I |  |
| Notes: <br> 1. Factory settings for SW1 and SW3 are OFF. <br> 2. It is recommended the WARN option not be set for alarm activation. If set for alarm activation, the alarm cannot be retired with the RESET button. The WARN alarm is associated with the 128 lamp. <br> 3. Only one LBO A switch can be in the up position at the same time, and only one LBO B switch can be in the up position at the same time. <br> 4. Set to TERM when SCIU is cabled into the live traffic bit stream in both directions to retime it. Set to BRDG only when the SCIU is to monitor the live traffic bit stream (cabled from DS1 through bridging resistors to EAST A IN only). |  |  |

Table I. SCIU SW4 0/ALI and OFF/OPII Section Settings

| SECTION | SETTING | RESULT |  |
| :---: | :---: | :---: | :---: |
|  |  | INPUT LOS | INPUT OOF |
| 0 / ALL 1 | OFF | LOS output | LOS output |
| OFF / OPT1 | OFF |  |  |
| 0 / ALL 1 | OFF | LOS output | Received signal output |
| OFF / OPT1 | ON |  |  |
| 0 / ALL 1 | ON | Framed all-ones output | Framed all-ones output |
| OFF / OPT1 | OFF |  |  |
| 0 / ALL 1 | ON | Framed all-ones output | Received signal output |
| OFF / OPT1 | ON |  |  |
| Note: Factory settings for SW4: 0 / ALL 1 and ESF/D4 to ON, TERM/BRDG and OFF/OPT 1 to OFF. |  |  |  |

Table J. Test Sign-off

| CHART \# | TEST | MASTER | EXP \#1 | EXP \#2 | EXP \#3 |
| :---: | :---: | :---: | :---: | :---: | :---: |

Place a check mark or initials beside each individual test after test is completed. When all tests have been completed, sign and date at the bottom.

Note: The test and acceptance procedures listed in this document are recommended guidelines. The Test Sign-off form is for customer use only. When completed, file locally, as per local company practice.

| 1 | Power Test |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 2 | Amplitude Verification Test |  |  |  |  |
| 3 | FA Card Test |  |  |  |  |
| 4 | AI Card Test |  |  |  |  |
| 5 | Cl or ACI Card Test |  |  |  |  |
| 6 | ST2 Card Test |  |  |  |  |
| 7 | ST2E Card Test |  |  |  |  |
| 8 | ST3E Card Test |  |  |  |  |
| 9 | ST3 Card Test |  |  |  |  |
| 10 | MCA/MCA-2 Card Test |  |  |  |  |
| 11 | Timing Output Card Test |  |  |  |  |
| 12 | SCIU Card Test |  |  |  |  |

DCD System Test and Acceptance completed by:
Date:
Comments:

